



AICTE MANDATORY DISCLOSURE

NAME AND ADDRESS OF THE INSTITUTION

Name	VIVEKANANDA INSTITUTE OF TECHNOLOGY
Address	GUDIMAVU, KENGERI HOBLI, KUMBALAGODU
City	BENGALURU
State	KARNATAKA
Website	www.vitb.ac.in
PhoneNo	+918028437036,7625013435
FaxNo	080-28437944
Email	vkitprincipal@gmail.com
AICTE PermanentID	1-12176161
Date	2026
Periodof Last Approval	2025-26
Longitude& Latitude	77.45407128020378,12.857224027622449
Collegehours	8.30 AM to 4.30 PM

NAME AND ADDRESS OF THE TRUST/SOCIETY/ COMPANY AND THE TRUSTEES

Name	JANATHA EDUCATION SOCIETY
Address	Vivekananda College Premises, Next to Orion Mall, Dr.Rajkumar Road, Rajajinagar II Stage
City	Bangalore-560 055
State	KARNATAKA
PhoneNo	080-23371952
Email	jesmanager@gmail.com

Janatha Education Society
Details of Trustees

Sl. No.	Name of the Trustee & Address	Designation	Mobile No.	Email
1	Sri. BALAKRISHNA H. C Amma Nilaya, No.21, (22) 1st Cross, 1st Stage MIG KHB Colony, Basaveshwara Nagara Bengaluru-560079	President	9448066939 9448050192 9663146939	jesmanager@gmail.com
2	Dr. R Ravindra Chairman Suguna Hospital 1A/87, Dr. Raj Kumar Rd, 4th 'N' Block, Udayam Nagar, Rajajinagar, Bengaluru, 560010 9845034546	Vice - President	9845034546	
3	Sri. Appa Reddy Harish #20/5 LV atmosphere 1 st Floor, Ali Asker Road Police Commissioner Back Gate, Vasanthnagar Bangalore - 560 0052	Secretary	9845042747	haagindia@gmail.com
4	Sri. G.K Sathyakeerthi No.402/1, First Floor 13th Cross, Sadashivanagar Bangalore - 560 080 9986577036 9742383826	Joint Secretary	9986577036 9742383826	
5	Sri. K.P. Muthaiah No.12, 1st Main, 3rd Stage Vinayaka Layout Vijayanagar, Bangalore - 40	Treasurer	9902144188	

6	Sri. Ganesh Reddy #.1253, 1st Tower Prestige West Wood Mysore Deviation Road Gopalapuram, Magadi Road Bangalore 560023	Internal Auditor	9108288622	
7	Sri. Balagopal H.G Shubha Shree' No,893 II Stage West Chord Road Bangalore-560086	Advisor	9845047495	hgbalagopal@gmail.com
8	Prof. G.K Narayana Reddy No. 622, 1 st Main Road, 2 nd Block, Rajajinagar, Bangalore – 560 010	Mentor	9448713712	
9	Dr. A.C Raghuram #98, 5A Cross, Dollar Colony, RMV 2nd Stage, Bangalore – 560 094	MC Member	9945560104	
10	Sri. Narayana Reddy I No.23/1,1 st A Main Road 5 th Cross, Yeshwanthpur Bangalore-560022	Member MC	9342868296	
11	Sri. Syama Raju K #150/B 10 th Main Road RMV. EXTN Sadashivanagara Bangalore-560080	Chairman VKIT	9844013391	
12	Sri. Manju K.P. No.95, 1st Floor, 4th Cross 2 nd Stage AGB Layout Mahalaksmipuram Bangalore-560096	Member MC	9844013391	

13	Sri. Anil G.V. No 44/2A PVR Swasti Kodigehalli Gate Sahakara Nagara Post Bangaluru 560094	Vice Chairman VKIT	9448663752, 9538849111	
14	Sri. K.P Raghunand 3rd Block, 3rd Stage Basaveshwara Nagar Bangalore - 560 079	Member MC	9448009995	
15	Smt. Sapna N #62, Sandeep's, 1st'A' Main 1st Block, Dollar's Colony 2nd Stage, Rajmahal Vilas Extn Bangalore - 560 094	Member MC	9945143401	
16	Sri. A.M Umashankar # 5/106, Sri Rama Nilaya 10th Main, 11th A Cross Malleshwaram Bangalore- 560003	Member MC	9845017319	
17	Sri. K.T. Nagaraja No.88/A, 12th Cross, 1st 'K' Block Kethamaranahalli Bangalore -560010	Member MC	9845157272	
18	Smt. Sulochana Jayaram No.33, 6th Main Road Malleswaram Bangalore - 560 003	Member MC	9845074474	
19	Smt. Pankaja B.H. No.38, 9th Cross, 8th Main, 1st Floor S.B.M. Colony, Brundavan Nagar Mathikere Extension Bangalore-560054	Member MC	9742818256	

20	Sri. M.R. Lakshmi Manohar No.169, 1st Floor Lower Palace Orchard Bangalore - 560 003	Member MC	7829748937	
21	Smt. S.N. Latha #1122, 9th Cross Adharsha Layout <i>Basaveshwaranagar</i> Bangalore -560079.	Member MC	9980932073 9845345391	
22	Sri. Anil Kumar S Flat No. 502, 5 th Floor, SLLR Mansion No. 18 (old) New No, 46/8, 12 th Main Road, 1 st Block Rajajinagar, Bengaluru- 560 010	Member MC	9731166014	

NAME AND ADDRESS OF THE PRINCIPAL

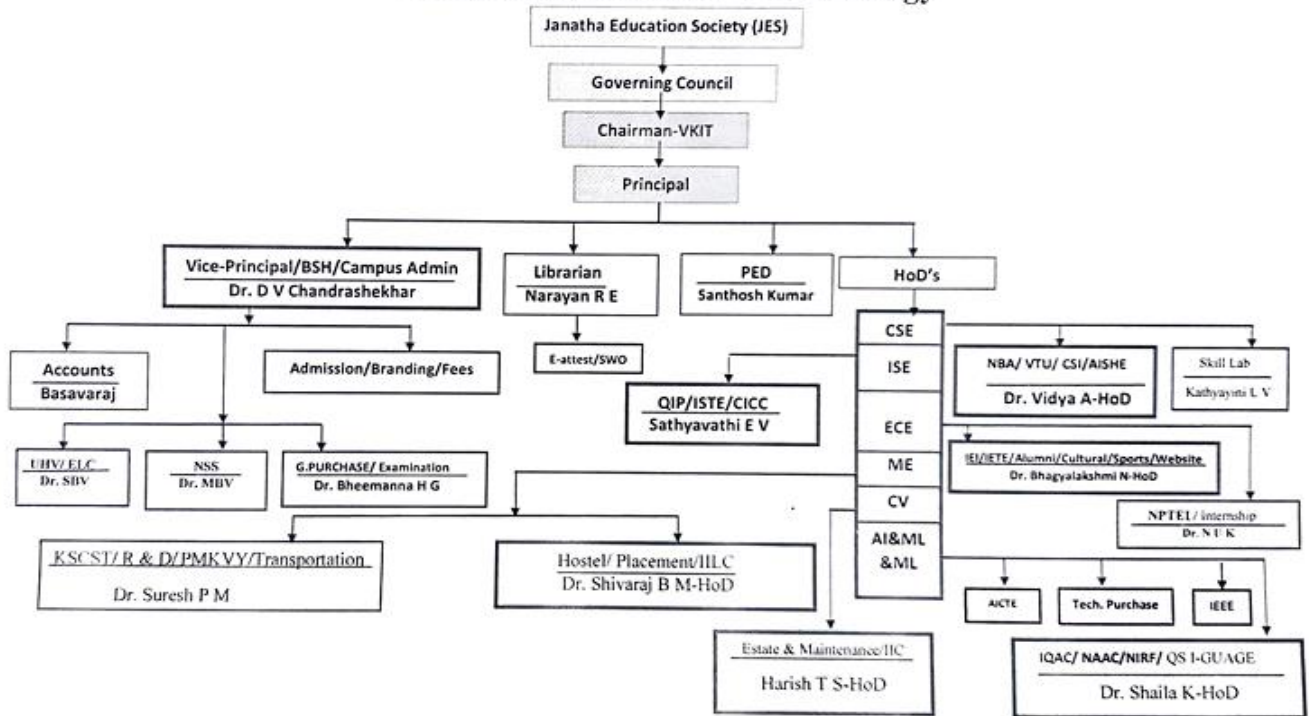
Name	Dr. K.M. Ravikumar Principal
Address	GUDIMAVU, KENGERIHOBOLI
City	BENGALURU
State	KARNATAKA
Website	www.vitb.ac.in
PhoneNo	080-28437696, 7625013435
FaxNo	080-28437696
Email	vkitprincipal@gmail.com

NAME OF THE AFFILIATING UNIVERSITY

Name of the University	Visvesvaraya Technological University, Belagavi-590 018, Karnataka
Name of Registrar	Dr. Prasad B Ramapure
Address	Jnana Sangama, Belagavi, Karnataka-590018
City	Belagavi
State	Karnataka
Website	https://vtu.ac.in
Phone No	0831-2498100
Fax No	0831-2405467
Email	registrar@vtu.ac.in

Organization Chart (Organogram)

Vivekananda Institute of Technology



Dr. Suresh P M
 27/01/2026
 Principal
 Vivekananda Institute of Technology
 Gudimavu, Kumbalagode, Kengeri Road,
 BANGALORE - 550074

Establishment of Anti-Ragging Committee

Sl. No.	Committee Members	Designation	Phone No's
1	Dr. K.M. Ravikumar Principal	President	9880373629
2	Dr. D.V.Chandrasekhar, Vice-Principal, Professor & HOD	Member	9448394326
3	Dr. Bhagyalakshmi N Professor & HOD, Dept. of ECE	Member	9480261129
4	Smt Sathyavathi E V HOD, Dept. of ISE	Member	8884050005
5	Dr. Vidya A Professor & HOD, Dept. of CSE	Member	9986011084
6	Dr. Shivaraj B.W. Associate Professor & HOD, ME	Member (Co-ordinator)	7338614265
7	Prof. Harish T S Professor &HOD,CE	Member	8197942065
8	Dr. Shaila K Professor & HOD, Dept. of AI & ML	Member	9964579958
9	Mr.Santhosh Kumar PED	Member	8880524787
10	Jyothi Girls Hostel Warden	Member	9901641513
11	Mr. Raghavendra Boys Hostel Warden	Member	6363481767
12	Police Sub Inspector, Kaggalipura Kanakpura Road, Bangalore	Member	080-28435250
Student Members			
1	Mr. Hemanth D.P. VII Sem ECE	Member	6363610472
2	Miss. Bhoomika R VII Sem AI & ML	Member	8073086624
3	Mr. Akash M VII Sem ME	Member	9845702287
4	Mr. Amith, VSem CSE	Member	8660403159
5	Miss. Niharika P, VI Sem ISE	Member	6362711022
6	Mr.Manoj G N VI Sem ISE	Member	7411841511
7	Miss Manyatha Vth Sem	Member	8088356296
8	Mr.Nithin Gowda M S VII AI &ML	Member	8310790963
9	Misss Deepthi., V Sem CSE	Member	9019556882
10	Miss Anusha U V CSE	Member	6362664394

Grievance Redressal Committee

Sl. No.	Committee Members	Designation	Phone No's
1	Dr. K.M. Ravikumar Principal	President	9880373629
2	Dr. D.V.Chandrasekhar, Vice-Principal, Professor & HOD	Member	9448394326
3	Dr. Bhagyalakshmi N Professor & HOD, Dept. of ECE	Member	9480261129
4	Smt Sathyavathi E V HOD, Dept. of ISE	Member	8884050005
5	Dr. Vidya A Professor & HOD, Dept. of CSE	Member	9986011084
6	Dr. Shivaraj B.W. Associate Professor & HOD, ME	Member (Co-ordinator)	7338614265
7	Prof. Harish T S Professor &HOD,CE	Member	8197942065
8	Dr. Shaila K Professor & HOD, Dept. of AI & ML	Member	9964579958
9	Mr.Santhosh Kumar PED	Member	8880524787
10	Jyothi Girls Hostel Warden	Member	9901641513
11	Mr. Raghavendra Boys Hostel Warden	Member	6363481767
12	Police Sub Inspector, Kaggalipura Kanakpura Road, Bangalore	Member	080-28435250
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3	Mr. Akash M VII Sem ME	Member	9845702287
4	Mr. Amith, VSem CSE	Member	8660403159
5	Miss. Niharika P, VI Sem ISE	Member	6362711022
6	Mr.Manoj G N VI Sem ISE	Member	7411841511
7	Miss Manyatha Vth Sem	Member	8088356296
8	Mr.Nithin Gowda M S VII AI &ML	Member	8310790963
9	Misss Deepthi., V Sem CSE	Member	9019556882
10	Miss Anusha U V CSE	Member	6362664394

Establishment of Online Grievance Redressal Mechanism

Online link for grievances of staff and Students:

grievancesvkit@gmail.com

Internal Complaint Committee

Sl. No	Position	Position in CICC	Gender	Mobile Number and E-Mail ID	Official Number
1	Smt.Sathyavathi E V Prof. Dept. of ISE	Chairperson	F	7899589186 sathyavathievs727@gmail.com	080- 28437696/ 28437036
2	Prof.PavitraS Assistant Prof. Dept. of Civil	Faculty Member	F	8344330879 pavithracivilian@gmail.com	
3	Prof. B.V.Shilpa Assistant Prof. Dept. of BSH	Faculty Member	F	9945959777 shilpabv@gmail.com	
4	Prof. Deepthi K Asst. Prof., Dept. of CSE	Faculty Member	F	9591376119 deepthik70@gmail.com	
5	Prof. Harshithaa M Asst. Prof. Dept of ECE	Faculty Member	F	9900005659 harshithaagowdam@gmail.com	
6	Mrs. Sunitha SDA-Office	Non- Teaching Member	F	9886659442 anandsunitha2004@gmail.com	
7	Mrs. Bhavya P.K. PA to Principal	Non- Teaching Member	F	9742851891 bhavyapk6@gmail.com	
8	Sakshi Dept. of AI & ML	Student	F	8310762022 holsamudresakshi@gmail.com	
9	Parvathi Revannavar	Student	F	8431578211 parvathirevannavar3@gmail.com	
10	Keerthana Dept. of ISE	Student	F	9483594331 yekeerthana@gmail.com	
11	K Swathi Dept. of ECE	Student	F	6363006164 kswathichikki@gmail.com	
12	Dr. Padmakshi Lokesh	NGO Member	F	9901926919 drpadmakshi@gmail.com	

SC/ST Committee

SL No	Name	Designation
1	Dr. Mohan Babu	Chairman
2	GangadharaiahH.N	Member
3	Suresh Kadenayankanahalli	Member
4	.Chandrika	Member

6. Programs

Name of the Programs approved by AICTE Under Graduate Programs

Sl. No	Course	Year of Est.	Approval Intake for the year (2025-26)
1	Electronics and Communication Engineering	1997	120
2	Computer Science and Engineering	1997	180
3	Information Science and Engineering	1998	120
4	Mechanical Engineering	2013	30
5	Civil Engineering	2013	30
6	Artificial Intelligence and Machine Learning	2020	90
Total Intake			570

Name of the Programs Accredited by NBA Under Graduate Programs

-NIL-

ACCREDITATION STATUS OF VARIOUS COURSES

Under Graduate Programs

Sl. No	Name of the Department	Accreditation Status			
		NBA		NAAC	
		From	To	From	To
1	Electronics and Communication Engineering	No		NAAC Accreditation B+	
2	Computer Science and Engineering				
3	Information Science and Engineering				
4	Mechanical Engineering				
5	Civil Engineering				
6	Artificial Intelligence & Machine Learning				

For each Programs the following details are to be given Under Graduate Programs

Sl. No.	Course	Intake	Duration (Year s)	Cut off marks/rank of admission during the last three years			Fees (As Approved by state Govt.)
				2025-26	2024-25	2023-24	
1	Electronics& Communication Engineering	60	4	L- 62443 H-225836	L-52959 H-185095	L-58833 H-158760	As per the Norms of State Government
2	Computer Science and Engineering	90	4	L- 33365 H-247415	L-33376 H-162917	L-34233 H-109691	
3	Information Science and Engineering	60	4	L-44542 H-243681	L-62118 H-154990	L-45801 H-134039	
4	Mechanical Engineering	30	4	-	L-518907 H-268642	L- H-197111	
5	Civil Engineering	60	4	L-189467 H-242374	L-199971 H-271117	L- H-202830	
6	Artificial Intelligence& Machine Learning	60	4	L-45463 H-189540	L-51980 H-271336	L-40443 H-133301	

Placement details for the Last Academic year

Year	Name of student who has been placed	Program graduated from	Year of graduation	Name of the employer with contact details	Pay package eat appointment (In INR per annum)
2024-25	Lakshmi Prasad V	ISE	2025	9113522849	4LPA
2024-25	Sumukha R Kashyap	CSE	2025	8073475514	4LPA
2024-25	CHANDAN K	CSE	2025	7676566584	4LPA
2024-25	Sanjay S J	ISE	2025	6366024023	4LPA
2024-25	Boya Lavanya	AIML	2025	9380867149	4-6LPA
2024-25	Shamita B V	AIML	2025	9986946027	4-6LPA
2024-25	Srusti S Deshmukh	AIML	2025	9113226365	4-6LPA
2024-25	Divya Shree D G	AIML	2025	7899843367	4-6LPA
2024-25	BennurKarthik	AIML	2025	8431930709	4-6LPA
2024-25	Bhavana B	AIML	2025	7975781990	4-6LPA
2024-25	Pratyushaa.K.R	CSE	2025	7899139841	4-6LPA
2024-25	Sri Gowri G	CSE	2025	8867248149	4-6LPA
2024-25	Jeevan R	CSE	2025	9620589505	4-6LPA
2024-25	Yashodha	CSE	2025	9110205145	4-6LPA
2024-25	Samridhi Raj Lakshmi	CSE	2025	7858063043	4-6LPA
2024-25	Deepak U	CSE	2025	9353146340	4-6LPA
2024-25	Pavithra S	CSE	2025	8618562005	4-6LPA
2024-25	Rajeev S	CSE	2025	8618701003	4-6LPA
2024-25	Abhishek.G	ISE	2025	9114098586	4-6LPA
2024-25	Mahesh A V	ISE	2025	8904644069	4-6LPA
2024-25	Lakshmi Prasad V	ISE	2025	9113522849	4-6LPA
2024-25	H Sripada Rao	CSE	2025	7619369147	4-6LPA
2024-25	Vishal V Nayak	ISE	2025	7676739832	4-6LPA
2024-25	Poorvika B C	CSE	2025	7411146168	4-6LPA
2024-25	KRUTHIKA Y N	CSE	2025	9606252591	4-6LPA
2024-25	Archana J	AIML	2025	7019433654	4-6LPA
2024-25	Gowtham D	CSE	2025	7019765319	4-6LPA
2024-25	Tejashree J Desai	ECE	2025	8123921040	4-6LPA
2024-25	Vishal V Nayak	ISE	2025	7676739832	6 LPA
2024-25	INSIA SARWATH	AIML	2025	9980797457	6 LPA
2024-25	Archana J	AIML	2025	7019433654	6 LPA
2024-25	Yashodha	CSE	2025	9110205145	6 LPA
2024-25	Manas	CSE	2025	7070399922	6 LPA
2024-25	Manas Kumar Pandey	CSE	2025	7070399922	2.8 - 10LPA
2024-25	Sumukha R Kashyap	CSE	2025	8073475514	2.8 - 10LPA
2024-25	Sanketh S Raykar	ISE	2025	8073373123	2.8 - 10LPA
2024-25	Shashank M S	AIML	2025	8123603099	2.8 - 10LPA
2024-25	Srusti S Deshmukh	AIML	2025	9113226365	2.8 - 10LPA
2024-25	Poorvika V C	CSE	2025	7411146168	2.8 - 10LPA

2024-25	Sri Gowri G	CSE	2025	8867248149	2.8 - 10LPA
2024-25	InsiaSarwanth	AIML	2025	9980797457	2.8 - 10LPA
2024-25	Bhavana A Ashrit	AIML	2025	7892997335	5-7 LPA
2024-25	Boyalavanya	AIML	2025	9380867149	5-7 LPA
2024-25	Tejeswini MJ	CSE	2025	6363711637	5-7 LPA
2024-25	Aditi Shenoy Y	ECE	2025	8197294887	5-7 LPA
2024-25	kavanacv c v	ISE	2025	8050186545	5-7 LPA
2024-25	Ranjan G	CSE	2025	72040 54312	3LPA
2024-25	GOVINDA NAIK	Civil Eng.	2025	6361894469	2LPA
2024-25	NANDANA N	Civil Eng.	2025	8217395339	2LPA
2024-25	Shalini P	CSE	2025	7204830958	4.2LPA
2024-25	Vilas U	CSE	2025	8147881947	4.2LPA
2024-25	Vandana GM	CSE	2025	6364400898	4.2LPA
2024-25	ShaziyaSulthana	CSE	2025	7411041267	4.2LPA
2024-25	Abhishek G	ISE	2025	9110498586	3.5LPA
2024-25	Bhavana A Ashrit	AIML	2025	7892997335	3.5LPA
2024-25	Bhavana B	AIML	2025	7975781990	3.5LPA
2024-25	Bhavana N S	ISE	2025	9380672262	3.5LPA
2024-25	Boyalavanya	AIML	2025	9380867149	3.5LPA
2024-25	Divya Shree D G	AIML	2025	7899843367	3.5LPA
2024-25	Inchara R	ISE	2025	8073833638	3.5LPA
2024-25	Milan Srinivas	CSE	2025	9513088030	3.5LPA
2024-25	Pooja C	ISE	2025	7204282792	3.5LPA
2024-25	Prajwal M	CSE	2025	9019648994	3.5LPA
2024-25	Rishi L	AIML	2025	8971665518	3.5LPA
2024-25	SahilRehman	AIML	2025	8050072105	3.5LPA
2024-25	Shamita B V	AIML	2025	9986946027	3.5LPA
2024-25	Sumanth S	ISE	2025	9019821481	3.5LPA
2024-25	Vandana GM	CSE	2025	6364400898	3.5LPA
2024-25	Nikhilsingh	CSE	2025	7007591541	3 LPA
2024-25	milansrinivas	CSE	2025	9513088030	3 LPA
2024-25	gowdaprajwal	CSE	2025	9019648994	3 LPA
2024-25	Vandana GM	CSE	2025	6364400898	3 LPA
2024-25	Mohammed Abu Talha	ISE	2025	9148074671	4 LPA
2024-25	Jeevan R	CSE	2025	9620589505	3-4.2 LPA
2024-25	Dhyan B	CSE	2025	9538446325	3-3.2 LPA
2024-25	Vandana GM	CSE	2025	6364400898	4.5LPA

Name and duration of Program(s)having Twinning and Collaboration with Foreign University(s) and being run in the same Campus along with the status of their AICTE approval.

Sl. No	Name of the University	Address	Website	Accreditation status of the University in Home Country	Ranking of the University in the Home Country	Whether the degree offered is equivalent to an Indian Degree	Nature of Collaboration	Conditions of Collaboration	Complete details of payment a student has to make to get the full benefit of Collaboration
1	NIL								

Dept. wise Faculty Details

Sl No	Name	Qualification			Designation	Experience in yrs		
		UG	PG	Ph.D		T	R	I
1	Dr. K.M. Ravikumar	B.E.	M.Tech	Digital Signal Processing	Principal	26	-	-
Electronics & Communication Engineering								
1	Dr. Bhagyalakshmi N	B.E.	M.Tech	Ph.D	Professor & HOD	23.3	8	-
2	Mamtha M	B.E.	M.Tech	Pursuing	Assistant Professor	5.3	1	-
3	Kavitha C.S.	B.E.	M.Tech	Pursuing	Assistant Professor	2.5	1	7
4	Monica N.P.	B.E.	M.Tech	-	Assistant Professor	2	-	-
5	Naveen Kumar N	B.E.	M.Tech	-	Assistant Professor	1.5	-	1
6	Arpitha S	B.E.	M.Tech	-	Assistant Professor	5	-	-
7	Anisha	B.E.	M.Tech	-	Assistant Professor	1.5	-	-
8	Raghunandan S	B. E	M.Tech	-	Assistant Professor	20		
9	Harshitha M	B.E.	M.Tech	-	Assistant Professor	2.5		
Computer Science and Engineering								
1	Dr. Vidya A	B.E	M.E	Ph.D	Prof. and Head	25	15	-
2	Dr. Sijin P	B.E	M.Tech	Ph.D	Associate Professor	8	-	
3	Deepthi T K	B.E	M.Tech	-	Assistant Professor	3	-	
4	Hanumatharayappa T A	B.E	M.Tech	-	Assistant Professor	18	-	
5	Shailja S	B.E.	M.Tech	Pursuing	Assistant Professor	3	-	-
6	Poornima Madalageri	B.E	M.Tech	-	Assistant Professor	1	-	1
7	Harshitha H S	BE	M.Tech	-	Assistant Professor	1	-	-
8	Mamatha C G	BE	M.Tech	-	Assistant Professor	1	-	-
9	Kathyayini LV	BE	M.Tech	Pursuing	Assistant Professor	5		
10	Swetha M	BE	M.Tech	Pursuing	Assistant Professor	2		
Information Science and Engineering								
Sl No	Name	Qualification			Designation	Experience in yrs		
		UG	PG	Ph.D		T	R	I
1	Mrs. Sathyavathi E V	B.E	M.Tech	-	Assistant Professor	2	-	-
2	Mrs. Suma N	B.E	M.Tech	-	Assistant Professor	2	-	-
3	Mr. Rahul J	B.E	M.Tech	-	Assistant Professor	1	-	-
4	Chandrakala V	B.E.	M.Tech	-	Assistant Professor	1	-	-
5	J Durga prasad	B.E.	M.Tech	-	Assistant Professor	10	-	-
6	Pradeep J	B.E.	M.Tech	-	Assistant Professor	20	-	-
AI & ML								
1	Dr. Shaila K	B.E	M.E	Ph.D	Professor and Head	23.2	16	-
2	Dr. Shailesh M L	B.E	M.Tech	Ph. D	Associate Professor	26	12	1

3	Dr. Nalaguru Udayakumar	B.Tech	M.Tech	Ph.D	Assistant Professor	17	-	-
4	Mrs. Jamuna H G	B.E	M.Tech	-	Assistant Professor	5	-	
5	Mr. Darshan T G	B.E	M.Tech		Assistant Professor	1.5	-	
6	Lavanya C	B.E	M.Tech	-	Assistant Professor	0.6	-	3
7	Sneha L S	B.E	M.Tech	-	Assistant Professor	0.6	-	
8	Eeranna T	B.E	M.Tech	-	Assistant Professor	0.4	-	
Civil Engineering								
1	Mr. Harish T.S.	B.E	M.Tech	Pursuing	Assistant Professor & Incharge HOD	12.5	0	0
2	Mrs. Pavithra S.	B.E	M.Tech	-	Assistant Professor	5	0	0
3	Mrs. Navya K.S	B.E	M.Tech	Pursuing	Assistant Professor	2	0	0
4	Mrs. Soubhagya S.B	B.E	M.Tech	-	Assistant Professor	2	0	0
Mechanical Engineering								
1	Dr. Shivaraj BW	B.E	M.Tech	Ph.D	HOD, Associate Professor & Placement officer	24	03	1
2	Dr. Suresh PM	B.E	M.Tech	Ph.D	Professor & Head – R&D	14	20	5
3	Prof. Harsha R N	B.E	M.Tech	Pursuing	Asst professor	10	-	-
4	Prof. Devaraju G P	B.E	M.Tech	Pursuing	Asst professor	10	-	2
Basic Science & Humanities								
1	Dr. D V Chandrashekhar	B.Sc	M.Sc	Ph.D	Professor HOD & Vice-Principal	28.6	20	-
2	Dr. H G Bheemanna	B.Sc	M.Sc	Ph.D	Professor	28.5	20	-
3	Dr. Shilpa B V	B.Sc	M.Sc	Ph.D	Associate Professor	16	9	-
4	Dr. Mohan babu V	B.Sc	M.Sc	Ph.D	Asst. Professor	11.5	13	-
5	Devika devi N	B.Sc	M.Sc		Asst. Professor	21	-	-
6	Lakshmikanth D B	B.Sc	M.Sc	Pursuing	Asst. Professor	11.8	05	-
7	Pooja D N	B.Sc	M.Sc	-	Asst. Professor	2	-	-
8	Thejaswini R	B.Sc	M.Sc	Pursuing	Asst. Professor	2	-	-
9	Sangeetha A N	B.Sc	M.Sc	-	Asst. Professor	1.5	-	-
10	Nischitha S	B.Sc	M.Sc	-	Asst. Professor	1.5	-	-
Kannada Teacher								
1	Vidya Vaman	BA	MA		Asst. Professor	28	-	-
English Teacher								
1	Shivanna C	BA	MA		Asst. Professor	08		

Electronics & Communication Engineering				
Sl No	Name of the Faculty	Qualification	Designation	Experience in years
1	H .M. Rajanna	Diploma	Foreman	25.7
2	Madhu H.D.	Diploma	Lab Instructor	1.2
3	Jai Kumar B	Diploma	Lab Assistant	11.5
4	M. Prashantha	SSLC	Office Assistant	11.6
Computer Science and Engineering				
1	Suresh. K	Diploma & B.E CSE	Programmer	20
2	Manjunatha. B.M	Diploma & BE CSE	System Analyst	15
3	Puneeth. D.J	Diploma in ECE	Lab Instructor	5
4	Lakshminarayana M.D	Diploma In CSE(B.Sc)	Lab Assistant	15
5	Thejashwini B	BE CSE	Lab Instructor	1
6	Bindhushree K	BE CSE	Lab Instructor	1
6	Ravindra H.C.	M.Com	Office Assistant	2.5 (in Sales)
7	Ramalinge Gowda R P	S S L C	Driver(attender)	27
Information Science & Engineering				
1	Govardhana G R	Diploma CSE	Lab Instructor	27.5
2	Krishnaveni	B E(M.Tech)	Lab Instructor	1
3	Baby Shalini D	B E	Lab Instructor	1
4	Chandrika P	B E(M.Tech)	Lab Instructor	0.7
5	Hithaishi M A	B E(M.Tech)	Lab Instructor	0.7
6	Jyothi C.B.	B.Sc	Office Assistant	1
Artificial Intelligence & Machine Learning				
1	Umesh R.H	B E(M.Tech)	Lab Instructor	0.6
2	Harsha C N	B E(M.Tech)	Lab Instructor	0.6
3	Sai Prakash	B E(M.Tech)	Lab Instructor	0.6
4	Monish M	B E(M.Tech)	System Analyst	1
5	Deepak M.S	Diploma in	Lab .Asst	0.4
Civil Engineering				
1	Arun M	Diploma	Lab Instructor	10.3
2	Shivappa H T	JOC in Surveying	Lab Assistant	10.2
3	Rajamudi B P	SSLC	Attender	20
Mechanical Engineering				
1	Mr. Mohan Kumar. D	Diploma in Mechanical Engg.	Lab Instructor	29
2	Mr. Kumar. K	N.T.C	Lab Assistant	29
3	Mr. Chandrashekar. K.C	N.T.C	Lab Assistant	29
4	Shivamma	(SSLC)	Sweeper	24
Basic Science & Humanities				
1	Puttaswamy Gowda A B	M. Sc	Lab Instructor	24
2	Shrunga H V	M. Sc	Lab Instructor	10
3	Kumar R	B Pharmacy	Lab Technician	26
Placement Dept.				
1	Arpitha R	MBA	Asst-Placement	0.6

Administrative Staff Details				
Library				
Sl No	Name of the Faculty	Qualification	Designation	Experience in years
1	Mr. R.E.Narayana	M.Sc, M.LISc.	Librarian	29
2	Dr. Manjuanth N	MLISc., M.Phil, Ph.D.	Asst. Librarian	18
3	Gayathri M	Diploma in Library Science	Library Asst.	22
4	Manjunatha B	SSLC	Helper	21.5
5	Sarajomma	(SSLC)	Sweeper	22.8
Admin/Accounts Dept.				
1	Basavarajappa Pated	B.Com, CA Inter	Senior Accountant	10
2	Sunitha H	M.Com	SDA	21.6
3	Govindaraju B	BA	SDA	10.1
4	Gangadharaiah H.N.	B.Com	Cashier	8
5	Prakash R	B.Com	Account.Asst	1
6	Nikhil Kumar V	ITI	Asst. Instructor	10
7	Narasimha R	ITI	Office Asst.	2
8	Bhavya P K	B.Com	P A Princepal	1
9	Deepa H	BCA	SDA	5
10	Kamamma T.H.	SSLC	Sweeper	22
11	Lakshmi Devi R	B.Com	Account.Asst	0.3
Estate office				
1	Dinakar	B.E. in Civil	Asst. Engineer	1
2	Kapane Gowda. K . B	SSLC, I T I, Electrical	Electrician	23
3	Guruprasad. S	SSLC, I T I, Electrical	Electrician	6
4	Venkatachlamurthy. T. H	SSLC	Helper	20
5	Ramesha. T	SSLC,HTC	Horticulture Asst	20
6	Krishna Murthy. C.K	BA	Attender	20
7	Mahalingaiah	-	Scavenger	20
8	Ramu	-	Gardner Asst	01
9	Amrit Kumar Swain	10 th Std.	Plumber	0.8
10	Shivanada S	Carpenter	0.8
Hostel Warden				
1	Ragavendra A	B'Com	Boys Hostel Warden	2
2	Jyothi J.V.	BA	Girls Hostel Warden	1.2
Transport				
1	Channaiah G Hiremath	7 th Std.	Driver	20
2	Raju . K	(SSLC)	Driver	16
3	Prakasha	SSLC	Driver	15
4	Dinesha. V	SSLC	Driver	14
5	Natesha . M . M	-	Driver	14
6	Putte Gowda	-	Driver	12
7	Govinda.D	SSLC	Driver	10
8	Gudda Chennaiah	SSLC	Driver	8
9	Rangappa	7 th Std	Driver	7
10	Somashekar. M	SSLC	Driver	2
11	Ravi. K	5 th Std	Driver	2
12	Krishna Murthy. N	SSLC	Driver	1.2

PERMANENT FACULTY: STUDENT-RATIO

SL NO.	Department	No. of Faculty	No. Of Students (II,III,IV Year)	Ratio
1	Electronics & Communication Engineering	10	245	24.5
2	Computer Science and Engineering	10	459	45.9
3	Information Science and Engineering	6	208	34.66
4	Mechanical Engineering	04	30	7.5
5	Civil Engineering	04	23	5.75
6	Artificial Intelligence and Machine Learning	08	266	21.28
7	Physics	03	-	
8	Chemistry	02	-	
9	Mathematics	05	-	
Total		54	1231	21.59

Profile of Principal

Name	Dr.K.M.Ravikumar
DateofBirth	19-06-1975
UniqueID	1SJEC0003124
WorkingExperience	Teaching: 25yrs Research Industry
Others	-
AreaofSpecialization	Electronics&CommunicationEngg.
Courses taught at Diploma/ Post Diploma/Under Graduate/Post Graduate/PostGraduateDiploma Level	Digital Signal Processing Microprocessor DigitalCommunication Signals & Systems
ResearchGuidance(No.OfStudents):	10Students
No. OfPaperspublished:inNational /International/Journals/Conferences	Journal National : 23 InternationalConferences-23 NationalConferences-8
No.OfBooksPublishedwithdetails (Name of the Book, Publisher with ISBN, Year of Publication Etc.	Automatic Detection of Syllable Repetition in Read Speech for Objective Assessment of StutteredDisfluencies”, PWASET, International Journal on Signal Processing, Vol.36, October 2008, pp. 270-273. “An Approach for Objective Assessment of Stuttered Speech Using MFCC Features”, ICGST, International Journal on Digital Signal Processing, Vol.9, June 2009, Issue.1, pp.19-24. “ <i>Temporal Dynamics of Repetitions during the Early Stage of Stuttering: An Acoustic Study</i> ” International Journal on Advanced Networking and Applications, IJANA, Vol.02, November 2010, Issue: 04, pp. 784-787. “Comparison of Multidimensional MFCC Feature Vectors forObjective Assessment of StutteredDisfluencies”InternationalJournalon Advanced Networking and Applications, IJANA, Vol.02, April2011, Issue:05, pp.854-860. “Stuttered Speech: An Acoustic Study” International Journal of Computer

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“Stuttered speech analysis using classification for objective assessment of early stutter”, Midas Touch International Journal of Commerce, Management and Technology, Volume 2, No. 1, January-2014, pp.62-66, ISSN:2320 -7787.

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“Decoding Baby Talk: Basic Approach for Normal Classification of Infant Cry Signal,” *International Journal of Computer Application*, 2015(1): 24-26, May 2015.
<http://www.ijcaonline.org/proceedings/icctac2015/number1/20921-2008>

“Energy Efficient VLSI Architecture for Image Enhancement Application” *International Journal of Applied Engineering Research* ISSN0973-4562 Volume 10, Number 20 (2015) pp 41413-41418 © Research India Publications.
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https://link.springer.com/chapter/10.1007/978-981-10-6698-6_10

“ An Algorithm to Detect Emotion States and Stress Levels Using EEG Signals”, *International Journal of Latest Research in Engineering and Technology (IJLRET)* ISSN:2454-5031 www.ijlret.com || PP.05-12,

	<p>December2017.</p> <p>http://www.ijlret.com/Papers/NC3PS2017/2.pdf</p> <p>“Region of interest based selective medical image encryption using multichaotic system” International Conference on Electrical, Electronics, Communication, Computer Technologies and Optimization Techniques (ICEECCOT-2017) in association with IEEE Bangalore section organized by GSSS Institute of Engineering Technology for Women, Mysuru held on 15th-16th December 2017.</p> <p>“Detection of Human emotions using features based on discrete wavelet transform of EEG signals” International Conference on Emerging Trends in Science & Technologies for Engineering Systems, Dept. of ECE/TCE/EEE, SJGIT, Chickballapur, 11th & 12th January 2018. (Scopus Indexed Journal)</p> <p>“Impulse Noise Cancellation in an OFDM system transmitting Medical Images using dual transform & geometric adaptive filter” International Conference on Emerging Trends in Science & Technologies for Engineering Systems (IJARTET) held on 11th & 12th January 2018.</p> <p>“Real Time implementation of Alertin and Tracking System for Chain Snatching” Published in e-journal-International Journal for Science and Advance Research in Technology (IJSART), May 2019.</p> <p>“Student risk identification learning model using machine learning approach” Published in International Journal of Electrical and Computer Engineering, October 2019.</p> <p>“Student Risk Identification Model Using Random Forest Algorithm” Published in European Journal of Molecular & Clinical Medicine (ISSN 2515-8260 Volume 07, Issue 08) December 2020</p>
Master	Completed
Ph.D	Completed

Projects CarriedOut	01
Patents(Filled &Granted)	01
TechnologyTransfer	-

FEE

DETAILS OF FEE, AS APPROVED BY STATE FEE COMMITTEE, FOR THE INSTITUTION

Time schedule for payment of Fee for the entire Programme

A.Y	Duration(Inmonths)
2025-26	Course Duration: 4 years, 8 Semesters (Fee will be collected every year during starting of Academic year. Permissible to pay within one month of starting of Academic year in special cases)
2024-25	Course Duration: 4 years, 8 Semesters (Fee will be collected every year during starting of Academic year. Permissible to pay within one month of starting of Academic year in special cases)
2023-24	Course Duration: 4 years , 8 Semesters (Fee will be collected every year during starting of Academic year. Permissible to pay within one month of starting of Academic year in special cases)
2022-23	Course Duration: 4 years , 8 Semesters (Fee will be collected every year during starting of Academic year. Permissible to pay within one month of starting of Academic year in special cases)

No.of Fee waivers granted with amount and name of students

A.Y	No. Of Students	Sanctioned Amount
2021-22	11	1,40,000
2022-23	20	1,40,000/-
2023-24	23	2,84,000/-
2024-25	29	3,68,000/-

Sl. No.	Name of the Student	USN	Concession Amount	Concession Given by
1	Kushwanth M.G.	1VK24EC024	5000	Secretary, JES
2	Minchana B.A	1VK24EC032	10000	President, JES
3	Vaishnavi P	1VK24CS120	10000	JES Order
4	Eshan Gowda	1VK24CS027	20000	Secretary, JES
5	Chandrika	1VK24CS020	10000	Secretary, JES
6	Pragathi N.D.	1VK24CS063	20000	Secretary, JES
7	Navya P	1VK24IS027	15000	Secretary, JES
8	Trisha A	1VK24IS074	10000	JES
9	Sahana H.S.	1VK24AI062	10000	President
10	Srinivasa R	1VK24AI071	10000	JES Order
11	Karnam Lathish	1VK24AI027	5000	JES Order
12	Sandhya D.N.	1VK24EC048	19000	JES Order
13	Ananya H	1VK23CS006	19000	JES Order
14	Anusha V	1VK23CS008	10000	Principal
15	Ashritha S Bhat	1VK23IS007	10000	JES Order
16	Hemanth Aradhya	1VK23IS018	10000	Secretary, JES
17	Kishore S	1VK23IS026	10000	Secretary, JES
18	Haridra A.B.	1VK23AI013	10000	Secretary, JES
19	Ullas R Gowda	1VK23AI056	10000	Secretary, JES
20	Sowmya S Jidagi	1VK22EC047	10000	JES Order
21	Shalini T.R.	1VK24EC043	10000	Secretary, JES
22	Kavitha S	1VK22IS024	20000	Principal
23	Kushal V	1VK21CS035	10000	Secretary, JES
24	Varun V	1VK21CS087	5000	JES Order
25	Pallavi R	1VK21IS032	10000	Secretary, JES
26	Kavitha S	1VK22IS024	20000	Principal
27	R.A Hemanth	1VK21IS039	10000	President, JES
28	Yashwanth N	1VK21IS037	5000	Secretary, JES
	TOTAL in Rs.		Rs. 3,68,000/-	

AcademicYear: 2023-24

Sl. No.	Name of the Student	USN	Total Demand	Concession Amount	Concession Given by
1	ANANDA B H	1VK23EC005	120000	10000	SECRETARY, JES
2	ARJUN P	1VK23EC009 (CET)	123265	10000	SECRETARY, JES
3	ANUSHA V	1VK23CS008	210000	10000	PRINCIPAL
4	ANANYA H	1VK23CS006 (CET)	123265	19000	JES
5	SHASHANK K N	1VK23IS047	175000	10000	PRINCIPAL
6	YASHASHWINI R	1VK23IS062	175000	10000	PRINCIPAL
7	MANASA N	1VK23IS029	160000	10000	PRESIDENT, JES
8	KISHORE S	1VK23IS026	160000	10000	SECRETARY, JES
9	HEMANTH ARADHYA	1VK23IS018	160000	10000	SECRETARY, JES
10	VARSHINI M C	1VK23IS059 (CET)	123265	10000	PRINCIPAL
11	NISARGA P	1VK23AI035	130000	10000	PRINCIPAL
12	POOJITHA U	1VK23AI037	130000	10000	PRINCIPAL
13	ULLAS R GOWDA	1VK23AI056	130000	10000	SECRETARY, JES
14	HARIDRA A V	1VK23AI013	150000	10000	PRINCIPAL
15	SHALINI T R	1VK22EC043	80000	10000	PRESIDENT, JES
16	SOWMYA S JIDAGI	1VK22EC047	80000	10000	PRESIDENT, JES
17	NIRANJAN N	1VK22CS049	180000	10000	PRESIDENT, JES
18	HARSHITHA K C	1VK22IS018	150000	10000	PRESIDENT, JES
19	KUSHAL V	1VK21CS035	140000	10000	SECRETARY, JES
20	VARUN V	1VK21CS087	100000	50000	SECRETARY, JES
21	PALLAVI R	1VK21IS032	100000	10000	SECRETARY, JES
22	SUNIL N	1VK18CV021	91500	5000	SECRETARY, JES
23	ARCHANA B	1VK20AI004	101500	20000	SECRETARY, JES
	TOTAL in Rs.			2,84,000	

Academic Year: 2022-23

Sl. No.	Name of the Student	USN	Total Demand (in Rs.)	Concession Amount (in Rs.)	Concession Given by
1	SHALINIR	1VK22EC043	80000	10000	JES
2	SOWMYASJIDAGI	1VK22EC047	80000	10000	JES
3	GAGANAKEERTHIG	1VK22CS024	170000	10000	JES
4	TEJASGOWDAM	1VK22IS057	70000	10000	JES
5	JAYANTHBV	1VK22IS021	150000	10000	JES
6	NIHARIKA P	1VK22IS038	150000	10000	JES
7	KEERTHANAKR	1VK22IS026	150000	10000	JES
8	NITHINGOWDAMS	1VK22AI025	120000	10000	JES
9	GOWTHAMS	1VK22AI013	120000	10000	JES
10	NAGENDRAM	1VK22ME005	55000	10000	JES
11	AKASHMP	1VK22ME004	55000	10000	JES
12	VARSHINIBN	1VK21EC023	80000	10000	JES
13	KUSHALV	1VK21CS035	140000	10000	JES
14	PALLAVIR	1VK21IS032	110000	10000	JES
Total				140000	

Academic Year:2021-22

Sl. No.	Name of the Student	USN	Total demand	Concession Amount	Concession Given by
1	HARSHITHKUMARH	1VK21EC012	80000	10000	JES
2	VARSHINIBN	1VK21EC023	90000	10000	JES
3	VARUNV	1VK21CS087	140000	40000	JES
4	KUSHALV	1VK21CS035	140000	10000	JES
5	MANOJV	1VK21IS025	110000	10000	JES
6	AMRUTHAS	1VK21CS003	90000	10000	JES
7	ARCHANAB	1VK20AI004	100000	10000	JES
8	DEEPASHREE R	1VK20IS010	100000	10000	JES
9	SAHANAN	1VK19CS040	90000	10000	JES
10	DARSHANC GOWDA	1VK19CS013	115000	10000	JES
11	SONUKCHRISTY	1VK18CS048	101500	10000	JES
Total				1,40,000/-	

Number of scholarships offered the Institution, duration and amount

A.Y	Numberof Scholarshipoffered	Duration(In Years)	Amount(Rs)
2021-22	21	1	3,15,000/-
2022-23	20	1	3,15,000/-
2023-24	26	1	3,90,000/-
2024-25	25	1	3,75,000/-

Estimatedcost of boarding and Lodging in Hostels

A.Y	Estimated cost of boarding and Lodging (Rs)
2022-23	Boy's Hostel –Rs. 65,000/- P.A Girl's Hostel-Rs. 60,000/ P.A
2023-24	Boy's Hostel –Rs. 65,000/- P.A Girl's Hostel-Rs. 60,000/ P.A
2024-25	Boy's Hostel –Rs. 85,000/- P.A Girl's Hostel-Rs. 65,000/ P.A
2025-26	Boy's Hostel –Rs. 80,000/- P.A Girl's Hostel-Rs. 80,000/ P.A

Admission

Number of seat sanctioned with the year of approval & No.of Students admitted under various Categories each year in the last three years

Programme Level	NameofProgramme/ Course	No. Of Seats Sanctioned For 2025-26	No. of students admitted		
			2025-26	2024-25	2023-24
UG	Electronics& CommunicationEngineering	120	50	57+06	57+1(SNQ)
	ComputerScienceand Engineering	180	157	115+08	75+4(SNQ)
	Information Science and Engineering	120	39	44+06	59+3(SNQ)
	Mechanical Engineering	30	04	07+01	3+1(SNQ)
	Civil Engineering	30	05	06+01	1+2(SNQ)
	Artificial Intelligence & MachineLearning	90	87	75+05	58+3(SNQ)
	Total	570	342	304+27	267
Ph.D	Electronics& Communication Engineering				0
	ComputerScienceand Engineering	-	-		-
	Mechanical Engineering	-			-
	CivilEngineering	-			-
	Mathematics	-			-
	Chemistry	-			-

Number of application received during last year for admission under Management Quota and Number of Students admitted

Application Received: 34

Admitted: 34

Admission Procedure

Mention the admission test being followed, name and address of the Test Agency/State Admission Authorities and its URL (website).

Admission test Name	Address	URL
KEA/CET	Karnataka Examination Authority, Sampige Road, 18 th Cross, Malleshwaram, Bengaluru -560 012 Phone No. 080 - 23460460	https://cetonline.karnataka.gov.in/kea/
COMED-K	#132, Second Floor, 11 th Main, 17 th Cross, Malleshwaram, Bangalore-560 055	https://www.comedk.org/

No. Of Students allotted different Test qualified Candidate separately (AIEEE/JEE/CET/State Conducted Test/University Test (CMAT)/Association conducted Test etc.

Admissions Current Academic year 2025-26

Branch	KEA	Comed-K	Total
Electronics & Communication Engineering	46	-	46
Computer Science & Engineering	111	-	111
Information Science & Engineering	29	-	29
Artificial Intelligence & Machine Learning	40	-	40
Civil Engineering	04	-	04
Mechanical Engineering	02	-	02
Total	232	-	232

Calendar for admission against Management Quota Seats

The probable date is from July to October (Based on the Availability of Seats)

Last Date of Request for Applications: 15.09.2025:

For the year 2025-26

Last Date for Submission of applicants: 15.09.2025

(As Per AICTE) for the year 2025-26

Dates for announcing final results—**As per the prescribed date announced from Competent Authority**

Release of admission list: **Main list and waiting list shall be announced on the same day**

Date of Acceptance by the candidate—**As per date announced by Approval authorities from time to time.**

Last date for Closing of Admission: As Prescribed by AICTE/VTU/GOK starting of the academic session: As Prescribed by AICTE/ VTU

Criteria and Weightages for Admission

Describe each criterion with its respective weightage i.e. Admission Test, marks in qualifying examination, etc.,

As per Govt. Norms

Mention the minimum Level of Acceptance, if any

As per Govt. Norms

List Admission under Management Seats/Vacant Seats

List of Candidates who have been offered admission

Artificial Intelligence & Machine Learning

1	AMRUTHA A H
2	AVINASH P S
3	BOYA LOKESH
4	CHANDAN KUMAR K
5	CHARANYA R
6	DHANUSH KUMAR G
7	DURAIRAJ V

8	HARSHANANDA N
9	HARSHITH D
10	JEEVANRAJ B
11	LIKITH C
12	MOULYA B S
13	NISHA B V
14	THANUSHREE K B
15	VIDUSHI JHA
16	VINNU H R
17	VISHNU PRIYA D
18	YASHWANTH P

Computer Science and Engineering

1	LAHARI M R
2	MOHAMMED ROOMAN PASHA
3	NIVEDITHA RAJ J
4	THARUN S N

Electronics & Communication Engineering

1	HARSHITHA
2	MAHESH M S
3	MITHUN U
4	POOJA KADADI
5	SANGAMESH
6	VISHAL

Mechanical Engineering

Sl. No	Name of the Student
1	DHANYASHREE
2	KARTHIK SAVANUR
3	KISHAN GOWDA N
4	SUHAS T

Civil Engineering

Sl. No	Name of the Student
1	INDIRA N
2	MALLINATH B S

Central Examination facility- Number of rooms and capacity of each Room with 30 Capacity

Online examination facility -Available

No.Of Nodes-200, Internet band Width etc.-300 Mbps

Barrier free Built Environment - Available

Fire and Safety Certificate - Not Available

Hostel facilities-Available

ComputingFacilities: Available

InternetBandWidth: 300MBPS

Innovation Cell: Available

Socia lMediaCell: Available

Games & Sports facilities -Available

Information of Infrastructure and Library:

●Number of Library books/Titles/Journals available (Programme-wise)

SL. No.	Branch	No.Books	No. of Titles	No.Of Journals
1	E&CE	10086	2192	Subscribed VTU Consortium 11261+
2	CS&E	5706	1552	
3	IS&E	4385	947	
4	ME	1233	395	
5	CE	1771	430	
6	AI&ML	669	237	
7	BSH	3388	757	
8	Donoated Book	159	20	
7	Govt. Books	288	70	

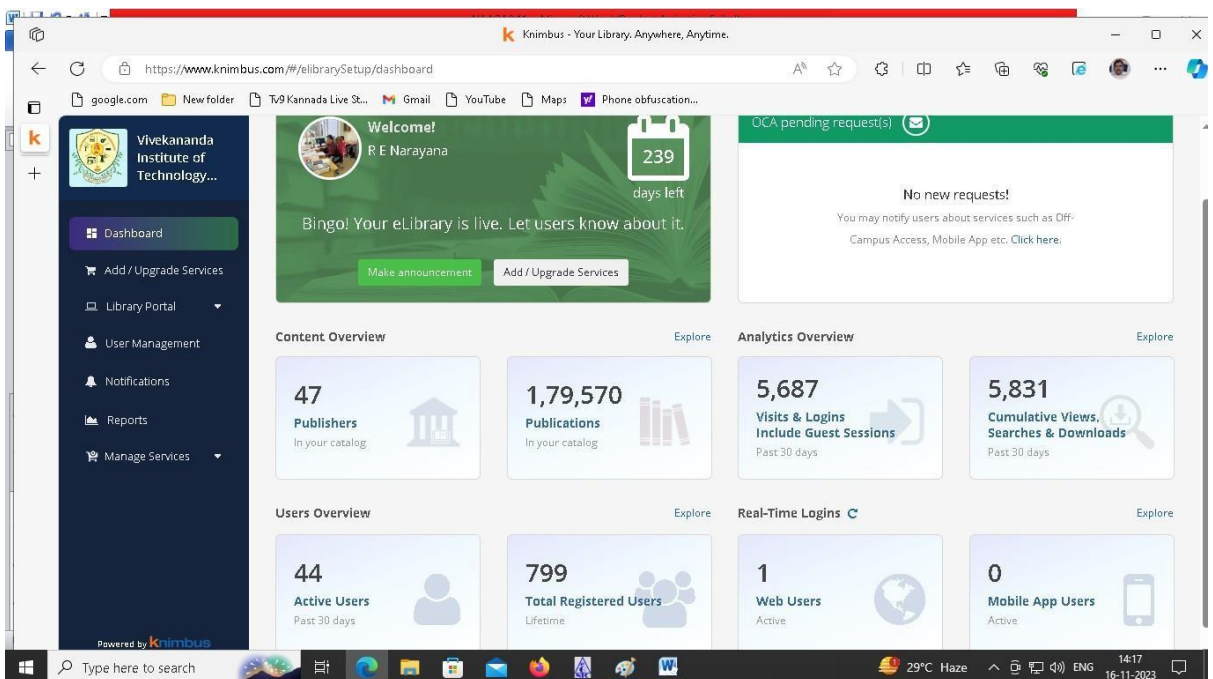
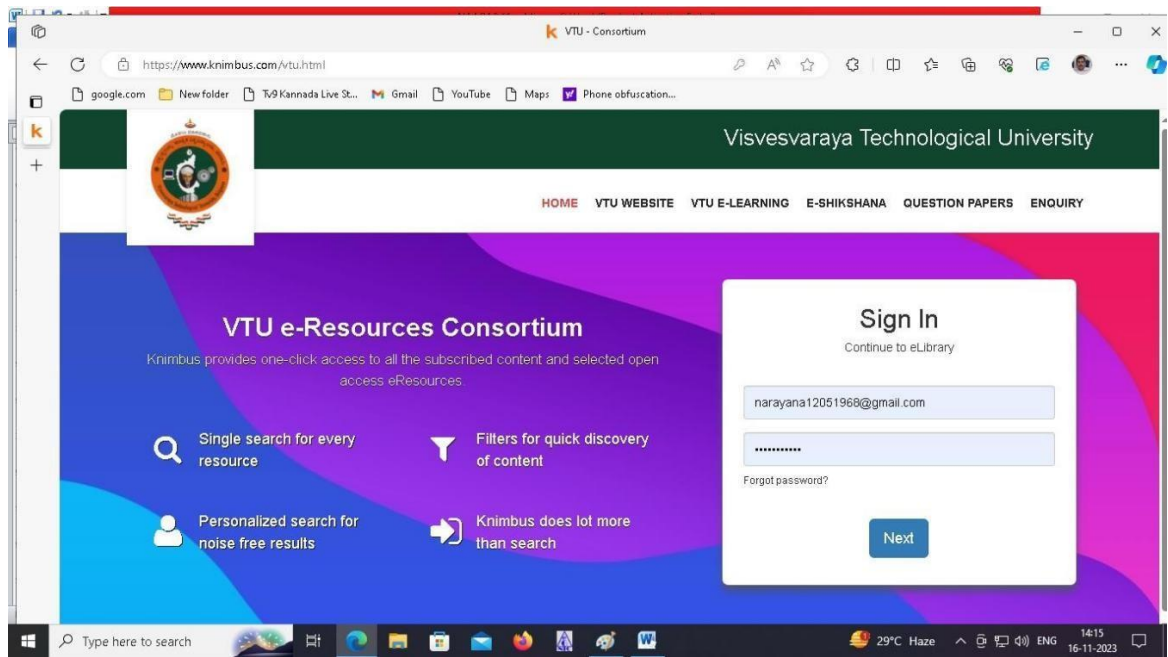
8	VTU Hand Book	847	04
Total		28532	6604

- List of online National/International Journals subscribed-

Through Subscribed VTU Consortium

- E-Library facilities

Yes, E-Library facilities available in VKIT library Through-K-Nimbus Digital library platform (Subscribed VTU Consortium)



A screenshot of a web browser displaying the homepage of the Vivekananda Institute of Technology (VIT) library system. The browser's address bar shows the URL `vnitb.knimbus.com/user#/home`. The page header includes the VIT logo, the text "Vivekananda Institute of Technology", and navigation links for "eCatalog", "Explore", "Research", and "About Us". A search bar is present with the placeholder text "Search for title, author, etc.". The main content area is titled "Section" and features two book covers: "WACHOs are dead... WMPs are no a hav..." and "None". Below this is a "Source" section with a "View All" link, displaying logos for "ACH Digital Library (ICVI)", "eBooks Premium", "Emerald insight", "ice Institute of Civil Engineers (ICE Visual...", "knimbus Open eBooks", and "knimbus Open Journals". The browser's taskbar at the bottom shows the Windows search bar, system tray with a temperature of 34°C, and the date 04/13/2022 at 2:57 PM.

Sl. No.	Department	LaboratoryName	Equipment/FacilitiesDetails	Remarks
	CIVIL ENGINEERING	BUILDING MATERIALS TESTING LAB	Universal Testing Machine–100 Tonne Capacity, Impact Testing Machine, Torsion Testing Machine, Brinnell’s Hardness Testing Machine, Vicker’s Hardness Testing Machine, Rockwell’s Hardness Testing Machine, Tiles Testing Machine, Sieve Shaker, Digital Oven, Fine Sieves Made of Brass 200mm dia (1 Set), Coarse Sieves of 300mm dia made of GI (3 Set), Pycnometer (8 No's), Graduated Cylinders (50ml, 100ml, 500ml, 1000ml), Metal Cylinder (3 No's), Tamping Rod (3 No's), Vernier Calipers (5 No's), Strain Gauge (1 No.), Deflectometer (6 No's), Metal Wire basket (5 No's), Polythene Wash Bottle (2 No's), Weighing Balance (6kg–2 No's, 10kg–2 no's, 15kg– 2 No's)	
		SURVEY LAB	Dumpy Level model DL-9 with stand (8 No's), Auto Level with stand (8 No's), Vernier Theodolite with stand (8 No's), Pentax Total station R-205NE with with Accessories (2 No's), Electronic Measurement Device (1 No.), Digital Planimeter (2 No's), Open crossstaff (8 No's), Optical Square (8 No's), Prism square (2 No's), Ceylon Ghat tracer (5 No's), Hand Level (1 No.), Surveyor Compass with stand (1 No.), Clinometer Compass with stand (1 No.), Invar tape-15mt. (1 No.), Synthetic tape (1 No.), Tape-30 mt. (12 No's), Metric Chain-30mt. (12 No's), Engineer's Chain (1 No.), Gunter	

			chain-10mt. (1 No.), Revenue chain-20mt. (1 No.), Cloth tape (2 No's), Levelling Staff-5mt. (10 No's), Steel band tape-20mt. (6 No's), Metalictape-15mt.(1No.), Peg(20No's),Boxsextant(1No.), Pantagraph (1 No.), Plane table (8 No's), Ranging rod (40 No's), Arrows(120No's)	
		APPLIED ENGINEERING GEOLOGYLAB	Rock & Mineral samples (80 No's), Geologicalhammers(5No's), Moh's scale of hardness kit (10 No's), Pocket knife (10 No's), Pocket lense (10 No's), Pocket Magnet (5 No's), streak plates (20 No's), Compass clinometer (2 No's), Streak Plates (20 No's), Geological and Mineral Atlas of India(1No.)	
		HYDRAULICS &HYDRAULIC MACHINERYLAB	Pelton wheel turbine (1 No.), Francis Turbine (1 No.), Kaplan turbine (1 No.), Venturiflume (1 No.), Venturimeter (1 No.), Jet vanes(1 No.), Nozzle Meter (1 No.), Pipe Friction Apparatus (1 No.),Reciprocatingpump(1No.), Multi stage centrifugal pump (1 No.), Notches, Major and minor losses Apparatus (1 No.), Bernoulli's theorem verification apparatus(1No.), Verticalorifice meter(1No.)	
		COMPUTER AIDEDDESIGN LAB	DELL Desktop Computer (25 No's), Dell T20 Edge Server (1 No.), Laserjet Printer (1 No.), MultifunctionPrinter-Scanner(1 No.), AutoCAD Software (25 No's),STAADProV8iSoftware (25No's),EmersonLiebertmake GXTMTSeries10KVAUPS (1 No.)withLeochmakeBatteries-12V, 65AH (20 No's), Epson Projector X18 (1 No.), Prolitewallmount ProjectionScreen-6'x4'	

			(1 No.)	
		GEOTECHNICAL ENGINEERING LAB	Triaxial Shear Apparatus (1 No.), LiquidLimitDevice(Motorized)(3 No's), Shrinkage Limit Apparatus (3 No's), Direct Shear Apparatus (Motorised) (1 No.), Unconfined Compression Test Apparatus (Motorised) (1 No.), Fine Sieves Made of Brass200mm dia (1 Set), Coarse Sieves of 300mm dia madeofGI(1Set),Pycnometer(5 No's), Consolidation Apparatus (Single Gang) (1 No.), Universal Permeability Apparatus (1 No.), VaneShearApparatus(Motorised) (1 No.), Compaction Apparatus (2 No's), Sand Pouring Cylinder (Small) (3 No's), Soil Extruder(Rack Type) (2 No's), CBR Apparatus(Motorised) with 10kN Proving Ring and Dial Gauge (1 No.), Proctor Penetrometer (Spring Type) (2 No's), Swell Test Apparatus (1 No.),ThinWalledSamplingTubes (2 No's), Digital Sieve Shaker (1 No.), Permeability Apparatus (VariableHead)(1No.),SoilCone Penetrometer (3 No's), Plastic LimitDevice(3No's)	
		CONCRETE AND HIGHWAY MATERIALSLAB	Vicat needle apparatus(3 No's), Le-chartlier's mould(1 No's), Cementautoclave(1No.),Flexure testing M/C (1 No.), Blaine's air permeability apparatus (2 No's), Slump test apparatus (3No's), Compacting factor apparatus (1 No.), Vee Bee consistometer (1 No.), Permeability apparatus(1 No's), Vibrating table (1 No.), Vibrating machine(1 No.), Laboratoryconcretemixer(1No's), Accelerated curing tank (1 No.), Cube moulds (36No's), Beam mould(12No's),Cylindermoulds	

			(6No's),Measuringcylinders(12 No's),Densitybottle(6No's),Le-chatlierflask(10No's),Spatula(9 No's), Trowels (9 No's), Shovels(9 No's), Bowls(9 No's), Toolkit(2No's),GItray(12No's), Cylindrical metal measure (3 No's), CBR Apparatus(1 No's), AggregateCrushingTest(3No's), Los Angeles Abrasion Machine (1No.), Aggregate Impact Value Apparatus (1 No.), Length Gauge(2No's),ThicknessGauge (2 No's), IS Sieves (Price Each Size Sieve)(3 No's), Wire Basket(3 No's), Specific gravity bottle for Bitumen (5 No's), Automatic Penetrometer (1 No.), Ductility Testing Apparatus (1 No.),RingAndBallApparatus(1 No.), Flash And Fire Point Test Apparatus (2 No's), Tar Viscometer (1 No.), Oven Universal (1 No.), Electrical coil stoves(1No.),HotPlate(1No.).	
		ENVIRONMENTAL ENGINEERING LAB	Aerator(1 No.), Jar Test apparatus(1No.), pH meter Digital(1No.), pH meter (1No.), Electronic weighing balance(1 No.),Imhoffsettlingcone(2No's), Digitalconductivitymeter(1No.), Digitaldirectreadingconductivity meter(1No.),Magnetic stirrer (1No.),Magnetic stirrer digital (1No.), Stop watch(1No.), Nessler's cylinder (1No.), Desicator plain(1No.), Water bath Digital(1No.), Nephelometer (1No.),Spectrophotometer (1No.), Bunsen burner with stand (15 No's), Test tube handler(15 No's),Spatula (20 No's), Distilledwaterplant(1No.),Glass fiber filter disc(1No.),Conical Flask(120No's),Burette(60No's),	

			Pipette(40 No's), Beaker (90 No's), Test tube rim(150 No's), Funnel (30 No's), BuretteStand(30 No's), Glass Rod(40No's), Reagent Bottles(30 No's), BOD Bottles(20 No's), Reflux Flask (15 No's), Heating Mantle (15 No's), Nessler's tube (30 No's), Fermentation Tube(3 No's), Platinum Loop holder(6 No's), Wash bottle(30 No's), Durham's Tubes (1No.), Muffle Furnace(1No.),MeasuringJar(45 No's),Evaporating Dishes(20 No's), Basin Evaporating(1No's), BODIncubator (1No.), Hot Air Oven(1No.),DOmeter(1No.),Dig Flame Photometer(1No.), Petri dish(10No.).	
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Sl. No.	Department	Laboratory Name	Equipment/FacilitiesDetails	Remarks
01	Mechanical	Design lab	BifilarSuspension	
			CompoundPendulum	
			SpringMassSystem	
			TorsionalVibrationsetwithSingleRotorSystem	
			TorsionalVibrationsetwithDoubleRotorSystem	
			ForcedVibrationSystem	
			SingleDegreeVibrationsystem	
			JournalBearingTestRig	
			WhirlingOfRotatingShaffTestRig	
			Gyroscope	

		CombinedPolarizedscope	
		BalancingofRotars	
		Porter,Prorell,WattHartnerGovernor	
		StrainGaugeCurved beam	
		SimpleGeartrainWithHousing Bearing	
		EpicyclicGearBoxwithHousing	
		Modeltesting WithAccessories	
	Energy Conversion lab	AbleFlash&FirepointApparatus	
		PenskyMartinFlash&FireApparatus	
		RedwoodVisco meter	
		SayBoltviscometer	
		DigitalBombcalorimeter	
		BoysGascalorimeter	
		Planimeter	
		ValvetimingDiagram	
		PorttimingDiagram	
		4strokesinglecylinderKirloskarMakeNew Diesel Engine Test Rig	
		2strokesinglecylinderpetrolEngineTestRig	
		4strokesinglecylinderHONDApetrolEngineTest Rig	
		MulticylinderpetrolEngineofAmbassadorcar Test Rig	
		TorsionViscometer	

			TwostrokeTVSsecondHandBike	
			RingspannersetTAPARIA	
			Doubleendedspannersetandcuttingplier	
		F&FLab	Sheartestattachment	
			Sieveanalysissteatsetup	
			Rapiddrier	
			Singlepiecepatternsteppedpulley(AL)	
			Splitpatterngroovedpattern(AL)	
			flatters	
			Universalsandtesting M/C	
			Permeabilitytester	
			Corehardnesstester	
			Mouldhardnesstester	
			Claycontenttester	
			Moisturecontenttester	
			Sandrammerwithcoreboxes	
			Digitalweighingbalance	
			Mouldingboxes12*12*4	
			Doublesplitpattern(AL)	
			Matchplatepattern	
			Toolkit	
			shovels	

			Swageblock50kg	
			Sledgehammer6LB	
			Sledgehammer8LB	
			Anvil 100kg	
			poker	
			Greensand(bentonite+coaldustmixed)	
			Bentonitepowder	
			Roundtong	
			Flattong	
			Bendtong	
			Greensand	
			Swagetop 1/4''MShandle	
			Swagebottom1/4''MS handle	
			Hotchisel	
			Coldchisel	
			Teakwoodleveler	
			Hammerdoublehead3LB	
			ABCcoreoil	
			Pullertopandbottom	
			Bearingselfcorepattern	
			Peltonwheelcup	
			GIbucket20ltr	

			Bandy	
			Dusttrowelmetal	
			Plasticbucket5ltr	
		HMTLab	DeterminationofThermalConductivityofa Metal Rod.	
			Determinationofoverallheattransfer coefficient of a composite wall.	
			Determinationofeffectivenessonametallc fin.	
			DeterminationofHeatTransferCoefficientin a Free Convection on a vertical tube heat	
			DeterminationofHeatTransferCoefficientin a Forced Convection.	
			DeterminationofEmissivityofaSurface.	
			DeterminationofsteffenBoltzmannconstant.	
			DeterminationofLMDTandeffectivenessina parallel Flow and counter flow Heat Exchanger.	
			ExperimentsonBoilingofLiquidand condensation of vapour.	
			PerformanceTestonVapourcompression Refrigeration.	
			ExperimentonTransientconductionHeat Transfer.	
			PerformanceTestonAirconditioner.	
		Machine shopLab	Hicut3503allgearedheadstocklathe,ABC- 1000mm	
			Hicut3503allgearedheadstocklathe,ABC- 600mm	
			Revolvingcentre MT-3	
			Drillchuck½"	

			Powerhacksawblade	
			Centregauge60°	
			½”diewithhand	
			divider	
			divider	
			Ringspanner set	
			Doubleendedspannerset	
			HSS drill bits Size:4,6,8,10,12,14,16,18,20,22,25mmdia	
			HSSdrill bits Size:4,6,8,10,12mmdia	
			HSSdrill bits Size:14,16,18,20,22,25mmdia	
			Drillsleeve	
			DrillsleeveMT2-3	
			Centrebit	
			Boringbar5/16”	
			Allenkeyset1.5mm-10mm	
			Allenkeyset1/16” to3/18”	
			Pipewrench-18”	
			Screwspanner10”	
			Ballpeenhammer 800gms	
			Nylonhammer	
			LetterPunch6mm	
			Numberpunch6mm	

			Centre punch	
			Adjustable surface gauge 12"	
			2046 S dial gauge	
			Dial stand	
			Surface plate	
			Vee-Block	
			Try-square-6"	
			Micrometer-0-25mm	
			Vernier Calliper 0-150mm	
			Screw pitch gauge	
			Stand for surface plate-18" plate	
			Turning tool-RH	
			Side and face milling cutter dia-100*10*25.4	
			Vernier height gauge 300mm	
			Safety goggles	
			Radial drilling machine	
			8" power hacksaw machine	
			12" shaping machine	
			Universal milling machine 26"*8"	
			Bench grinder	
			Straight nose tool	
			Turning tool right hand	

			HSS tool bits: 5/16" * 5/16" * 4"	
			HSS tool bits: 5/16" * 5/16" * 6"	
			Grinding wheel 200mm Dia Smooth rough	
			HI-cut 3503 all geared lathe ABC-1000mm	
			HI-cut 3503 all geared lathe ABC-600mm	
			HI-cut 3503 all geared lathe ABC-600mm	
			HI-cut 3503 all geared lathe ABC-600mm	
		MMMLab	Pressure Gauge	
			Thermocouple	
			LVDT	
			Load Cell	
			Strain Gauge	
			Stroboscope	
			Profile Projector	
			Sine Centre	
			Sine Bar	
			Taper Gauge Plain	
			Angle Block Universal	
			Angle Gauges	
			Lathe Tool Dynamometer	
			Drill Tool Dynamometer	
			Slip Gauge Box	

			MechanicalComparator,Indianmake	
			OpticalFlats	
			GearToothVernier	
			GearToothMicrometer	
			ScrewThread(FloatingCarriageMeter)	
			MechanicalComparator	
			MagneticStandWithDialGauge	
			V-Block2''	
			VernierCaliper	
			Micrometer	
			GraniteSurfacePlate	
			DialIndicator	
			Bore Gauges	
			PrecisionSpritLevel	
			TaperPlugGauge	
			TaperRingGauge	
			PlainPlug Gauge	
			PlainRingGauge	
			FeelerGauge	
			PitchGauge	
			Radius Gauge	
			VernierHeightGauge	

			Roller Set	
			SnapGauge	
			i)0-6mm	
			ii)6-13mm	
			iii)13-19mm	
			SurfacePlate-CI	
			BevelProtractor	
		MT Lab	SingleDiscpolishingMachine	
			RectangularMuffleFurnace	
			WearTestingMachine	
			UltrasonicFlawDetector	
			Magneticcrack Detector	
			DyepenetratedApparatus	
			DigitalweighingBalanceLc:0.001 gm	
			ImpactTestingMachine	
			RockwellHardnessTestingMachinewithstd specimens	
			BinocularMicroscopewithTwostandard specimens(HCS & HSS specimen)	
			BrinellHardnessTestingMachinewithstd specimens	
			UniversalTestingMachineUTN-100knwith standard attachments of Tension test, shear test, Bending test & compression test	
			TorsiontestingMachine cap100Nm	

			Vickers Hardness testing Machine with std specimens	
			Pycnometer	
			Sieve shaker	
			Digital oven	
			Tile testing machine	
			Strain gauge indicator	
			Sieve for coarse aggregate	
			Sieve for fine aggregate	
			Bulk density for coarse aggregate	
			Bulk density of soil	
			Weighing Balance 3kg	
			Weighing Balance 5kg	
			Weighing Balance 15kg	
			Vernier caliper	
			Measuring Cylinder	
			Deplectometer 0-25 LC-0.01	
			Deplectometer 0-25 LC-0.01	
			Deplectometer 0-50 LC-0.01	
		Workshop Lab	Welding Transformer	
			Welding Hand Shield	
			Tongs	
			Hand Gloves	

			C-clamp	
			ElectrodeHolder	
			WeldingCable	
			WeldingGlass	
			ChippingHammer	
			PlainGoggles	
			Apron	
			Wire Brush	
			EarthingClamp	
			Lugs	
			MeasuringSteeltape	
			BenchVice	
			WeldingTable	
			Weldingplainglass	
			Cuttingplier	
			Powerhacksawmachine	
			Ballpeinhammer	
			BenchVice	
			PipeVice	
			Surface Plate	
			VernierHeightGauge	
			Hacksaw Frame	

			SurfaceGauge	
			FlatFile(Rough)	
			FlatFile(Smooth)	
			Square File(Rough)	
			Square File(Smooth)	
			RoundFile(Rough)	
			RoundFile(Smooth)	
			HalfRoundFile(Rough)	
			HalfRoundFile(Smooth)	
			Triangularfilerough	
			Triangularfilesmooth	
			Knifedgefilerough	
			Ballpeinhammer	
			Flatcoldchisel	
			Crosscutchisel	
			Coldchisel	
			Crosscutdiamondchisel	
			Trysquare	
			Outsidecaliper	
			Insidecaliper	
			Dividers	
			VernierCaliper	

			Micrometer	
			DrillingMachine	
			Grindingmachine	
			Tappingsets 6mm,8mm,10mm	
			Tapwrench10mm	
			Dieset 6mm,8mm,10mm	
			Diestock	
			Angleplate	
			Parallelbars	
			Scribers	
			Scrapers	
			Sledgehammer	
			Vblock	
			Cuttingpliers	
			SteelScale6inch	
			SteelScale12inch	
			Drillbits 6dia,5dia,8dia,10dia 12dia,14dia	
			Centerpunch	
			Drillvise	

			Numberpunch	
			Letterpunch	
			AdjustablewrenchBrush	
			SteelTape	
			Screwdriver	
			Spannerset	
			Oilcan	
			Jemper	
			Grindingwheeldresser	
			Legwise	
			Anvil	

Sl. No.	Department	Laboratory Name	Equipment/FacilitiesDetails				Remarks
1	ECE	Electronic Devices and Instrumentation Lab	EQUIPMENT SNAME	MAKE	SPECIFICATION	QTY (NOS)	
			Analog Ammeters		0-15-30mA	10	
			Analog Ammeters		0-100-200mA	10	
			Analog Ammeters		0-100-200micro A	05	
			Analog Ammeters		0-500microA	05	
			Powersupply	Unitron	0-30v/2ASingle	10	

			Powersupply	Unitron	0-30v/2A dual	10		
			Powersupply	Unitron	0-300v/1ASingle	02		
			Powersupply	Unitron	+/-12v or +/-15v	10		
			Digital multimeter	Motwane		06		
			Digital multimeter	Motwane		02		
			Digital Auto range multimeter	Fluke		01		
			Analog Oscilloscope	scientific		14		
			LCRQ Bridge	scientific		01		
			Function generator	Tektronic		12		
			Digital multimeter	Tektronic		10		
			Function generator	scientific		05		
			5kv voltage stablizer			01		
			DRB			10		
			DIB			10		
			DCB			10		
			Digital voltmeter		0-2v-20v bench type	08		
			Digital voltmeter		0-20v-200v	08		
			Digital Ammeter		0-200microA-2000microA	08		
			Digital		0-20mA-200mA	08		

			Ammeter				
			Digital Ammeter		0-200mA-2000 mA		08
			Powersupply	Uday	0-30v/2ASingle		10
			Powersupply	Uday	0-30v/2Adual		05
			Fixedsupply	Uday	+/-12vor+/-15v		10
			Digital & Analog IC Tester				01
			Digitalstorage oscilloscope	Akademik			07
2	DSD lab	EQUIPMEN TNAME	MAKE	SPECIFICA TION	QTY (NOS)		
		Digital IC trainerKit	Model4002		10		
		Analog IC trainerKit	Model5002		04		
		DRB		6 dials	05		
		DCB		5dials	05		
		DIB		5 dials	05		
		Digital multimeter	Motwane		05		
		Digital IC trainerKit			14		
		MPBased Analog IC Tester			01		
		MPBased Digital IC Tester			01		
		Function generator	scientific		05		

			Pulse generator			02		
			Fixeddc supply		+/-12vor +/-15v	10		
			Voltage stablizer		5KVA	01		
			DcPower supply		0-30v/2A	02		
			Fixedsupply		+/-12vor +/-15v	02		
			Digital IC trainerKit			12		
			Linear/Analog IC trainer kit			05		
			Digital IC Tester (Handled)			01		
			Digital Multimeter	Meco		03		
			DRB		6dials	04		
			DCB		6dials	04		
			DIB		6dials	04		
			Function generator	systronics		01		
			Singlepulse generator			01		
			computer	Dellvastro3268		10		
3		VLSI lab	EQUIPMEN TSNAME	MAKE	SPECIFICA TION	QTY (NOS)		
			Singlechip MCUTrainer	ESAMCB52		10		
			Powersupply	ESA psm2		10		
			Stepper motorwith			05		

			interfaceI-STEP				
			DCMotorI/F				05
			ElevatorI/F				05
			Dualdacl/F				05
			Lcdi/f				05
			CalculatorKB				05
			TSDR Interface				05
			Microwind software				50users
			PCI Based MSP430KITS				20
			Cadence software				20users
			ONLineUPS 5 KV				01
			HP ProlientML10 0server				01
			HPDesktop system Mtpc3330				20
			DellOptiplex380				04
			ALS –SDA ARMCTXM3 KIT	ALS			15
			UPS6KVA	Emersion			01
4		HDL Lab	EQUIPMENTS NAME	MAKE	SPECIFICATION	QTY (NOS)	
			Xilinxsoftware			01	
			Labview7.1			01	

			software				
			TMS320C6713 DSPKits	TMS 320C6713			10
			FPGA&CPLDKits				15
			Patterngenerator & logic analyzer				15
			32bitALU	32bit			01
			Spartern 6 daughtercard				10
			USBCablesforkits				10
			TMS320c6713DSP kits	TMS 320c6713			05
			Overhead projector				01
			Plasticprojection screen 5*5				01
			Vacuumcleaner				01
			Overhead projector				01
			Plasticprojection screen 5*5				01
			HPLaptop	hp			01
			Smartboard				01
			Multimedia projector				01
			Wallmountfor Multimedia projector				01
			E-podium				01
			Hitachiprojector	CP-X3021MN			01

			CP-X3021MN				
			HPcompaxdx2000 computers	HPcompax dx 2000			12
			WiproLQDSI5235 printer	WiproLQDSI 5235			02
			6Kva UPS	Emerson			01
			HPDX2480Model computer	HPDX2480 Model			30
			Sparten6daughter card				05
			USBcableforkits				05
			TpLINKN -300 Router	TpLINKN-300			01
			FPGAMother board				01
5		Advanced Communication lab	EQUIPMENTS NAME	MAKE	SPECIFICATION		QTY (NOS)
			Analog Signal sampling & Reconstruction unit	Kaushik enterprises			01
			Time division multiplexing Trainer	Kaushik enterprises			01
			Spectrum Analyzers	HAMEG	0.15MHZ TO 1050 MHZ		01
			Auto LCR Q Text	systronics			01
			Function Generator	systronics	10MHz		02
			Controlled Stabilizer	Powertronics	Powertronics 5 KVA		01
			Variable DC Regulated Power	Uday Engineers	0-30v/2A		04

			Supply				
			DualPowerSupply	Uday Engineers	0-30v/1A		03
			Analog Multimeter(Hung Chang)	Hung Chang			05
			DesktopDigital Multimeter	Meco	45p Multimeter		01
			DigitalMultimeter	Meco			02
			DecadeInductance Box	Uday	6Dials		06
			DecadeResistance Box	Uday	6Dials		06
			DecadeCapacitance	Uday	6Dials		06
			BandPass Modulator /Demodulator	FALCON			01SET
			Dual trace oscilloscope	Analog HM605 SCIENTIFI	60MHZ		03
			Analog pulse ModulatorTrainer	Digitronix			01
			AM/FM Transmittertrainer	Digitronix			01
			AM/FMReceiver Trainer	Digitronix			01
			FrequencyCounter	SCIENTIFI	1GHZ		01
			TDSDigital Oscilloscope	Tektronix			06
			AnalogICTrainer kit	UDAY			03
			DigitalICTrainer	UDAY			03

			kit			
			PatternGenerator	Electronics & Electronics product		01
			DecadeInductance Box	SIMS	6Dials	05
			DecadeResistance Box	SIMS	6Dials	05
			DecadeCapacitance Box	SIMS	6Dials	05
			Twentyfourtypes of antennas			24
			MultioutputPower Supply	Power Vision	+/- 5,12,15,v/	03
			MultiOutputPower Supply(Individual	Power Vision	+/-12,v	02
			DcRegulatedPower Supply	Power Vision	30v1 Amp Dual	06
			DcRegulatedPower Supply 3	Power Vision	30v2 Amp	06
			TDM Kit	scientech	ST (2102)	01
			ACAmmeter (Desktop)	EIC	0-50,100 ma	05
			DCAmmeter (Desktop)	EIC	0-25,250 ma	04
			Microprocessor basedICtester	TESTEL	DA40	01
			CRO	Scientific HM 203		04
			FiberOpticKit	scientech	ST2502	01
			Data Communicationand LANtrainer kit	Falcon		01

			SinglePulse Generator	Systronics		03	
			OpenRacksize5½ x 3 x12			02	
			Tablewithoutdraw			02	
			AnalogOscilloscope	LG5020	20MHz	05	
			100MHz Oscilloscope	LG5100	100MHz	02	
			1:1&10:1 combination compensatedprobe			08	
			DigitalMultimeter	meco	603	10	
			OFCKit(Link-B)	Falcon		01	
			DPSKKit	TETCOS		01	
			QPSKKit	TETCOS		01	
			NoiseGenerator	TETCOS		01	
			Bit Error MeasurementUnit	TETCOS		01	
			FunctionGenerator	FG806	(2MHz)	06	
			FunctionGenerator	FG811	(Am/FM)	03	
			SineWave Generator	HM5032	20MHz	02	
			DecadeResistance Box	PAN Electronic	5Dials	10	
			DecadeCapacitance Box	PAN Electronic	5Dials	10	
			DecadeInductance Box	PAN Electronic	5Dials	10	
			AnalogOscilloscope	EZ	HM5060 60MH Z	10	

			PulseGenerator	ST4063		13		
			OFC Kit	Scientech		01		
			DPSKKit	TETCOS		03		
			QPSKKit	TETCOS		03		
			PowerSupply	Power vision	(+/- 12V/*2A) +/- 15V/2A+/ -5V/2A	14		
			ElectronicProject Boards			10		
			SignalGenerator (Signal source)			02		
			Electronic IntegratedCircuit Kit					
			a) Coupled Directional Coupler			02		
			b) Ring Resonator			02		
			c) PowerDivider			01		
			d) AttenuatorPads- 3db,6db,10db			02	EACH	
			e) AdapterSMA(F) -N(M)			02		
			f) BNCCables			02		
			g) SMACables 1m& 45cms.			02		
			h) CoaxialMatch Load			02		
			i) Detector					

			Antennawith Accessories			02		
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			<p>a)Printed Dipole Antenna</p> <p>b) Rectangular Patched Antenna</p> <p>c)Ported Yagi Antenna</p> <p>d) Rotatable Antenna Stand</p> <p>e)Fixed Antenna stand</p> <p>f)VSWR</p>			02		
			UPSPB6000Batch: 01106VM0066UPS	EMERSON	6KVA	01		
			Batteries	EXIDE	42AH/12V	16		
			HitachiProjector cp-x3021	HITACHI	cp-x3021	01		
			Digitaloscilloscope	Scientific SMO 502	50MHZ	05		
			Signalgenerator	Physitech PHY-103 FAR	2MHZ	05		
6		DSP lab	EQUIPMENTS NAME	MAKE	SPECIFICATION	QTY (NOS)		
			UPSAdapt6KVA (1TA)	UPS System ITA Model	Batch: 210/20046 1211C010 0286KVA Single Phase of Single Phase	01		

			Battery RackwithLinks(Battery Stand)	Rocket Make	26Ah/ 12V 12V/ 65 AH/	16 01		
			COMPUTERS	DELL	Dell Optiplex(J M)380DT- N Series BaseIntel (R) core ™-2DUO Processor E 7500 (2.93GHz) 1066FSB/ 3M2 Cache. Integrated Broadcom (CBCM577 80)GB LAN 10/100/10 00,2GB(1 x 2GB) Non ECCDDR3 1333MHz SD RAM Memory 320 GB 7200 RPM 35" SATA Hard drive,16x Max DVD+1 – RW with duallayer write capable- ties for MT&DT,	07		

					DELLE 1912, 18.5"W HD Monitor with WLED , DELL™ MS111US BOptical Mouse, DELL™ KB212-B USBEntry 6-ness Keyboard (ENGLISH)		
			ROUTER	DIGISOL E	sl.no 003 YS CA000788 Mac :00177C1 F 27DA	01	
			Microprocessor Kit	ALS	8085 µp	10	
			Microprocessor	ALS	8086 µP	01	
			PowerSupply	ALS	(+12v,- 12v,+5v,+2 6v)	11	
			LogicController Interface Card	ALS		01	
			DualDAC&I/O InterfaceCard	ALS		01	
			ElevatorInterface Card	ALS		01	
			Keyboard/Display Interface Card	ALS		01	

			EPROM Programmer	ALS		01	
			InterfaceCard				
			8BitADCInterface Card	ALS		01	
			StepperMotor InterfaceCard	ALS		01	
			Basic Universal MicroprocessorKit	ALS	SDA-UNI - 01	10	
			PowerSupply	ALS	5v,1.5A	10	
			CPUCARD	ALS	UNI-85-8085	10	
			UNI-86-8086CPU Card	ALS	[UNI-31/51 8031/51 CPUCard (Inbuilt-8085 sl.No.13)]	10	
			StepperMotor Interface	ALS	ALS-NIFC - 01A	02	
			PowerSupply	ALS	5A,1A NIFC-01A-OPT-01	02	
			8Bit ADC	ALS	ALS - NIFC-07A	02	
			ElevatorInterface Card	ALS	ALS - NIFC-17	02	
			Keyboard/7 SegmentDisplay/ Display Interface	ALS	ALS-NIFC -09	07	
			LogicController Interface	ALS	ALS-NIFC -05A	07	

			StepperMotor Interface	ALS	ALS-NIFC-01A	06	
			StudyCard	ALS	ALS-	05	
			(USARI/Timer Interface)		NIFC-21 8251/8253		
			DualDACInterface	ALS	ALS-NIFC-06A	02	
			Microprocessor Trainer Kit	ALS	ALS-SDA-85	20	
			PowerSupply	ALS	5v,1.5A, +/- 12v,100 ma	20	
			KeyboardInterface Card	ALS		01	
			ElevatorInterface Card	ALS		01	
			LogicController Interface Card	ALS		01	
			StepperMotor InterfaceCard	ALS		01	
			PowerSupplyfor above Interface	ALS		01	
			DualDACInterface Card	ALS		01	
			24linesDigitalI/O Card with Timer (INSIDE-Dell Optiplex(JM) 380DT-NSeries SYSTEMS	ALS	ALS-PCI-07A	06	

			Desktop computers HpDX2480Intel core2 Duo	ALS	HpDX2480 Intelcore2 Duo	15	
7		Elect	EQUIPMENTS	MAKE	SPECIFICATI	QTY	
		rical lab	NAME		ON	(NOS)	
			Measurement of current,power andpower factorof incandescent lamp,fluoresce ntlampand LEDlamp	Pragna		02	
			Measurement ofResistance and inductanceof achokecoil using 3voltmeter method	Pragna		02	
			Determination ofphaseand linequantities inthree phase staranddelta connected load	Pragna		02	
			Measurement ofthree phase powerusing twowattmeter method	Pragna		02	
			2wayand3 waycontrolof lamp	Pragna		02	
			Measurement	Pragna			

			of earth resistance			02		
			Study of effect of open and short circuit in	Pragna		02		
			simple circuit					
			Wheat stone bridge KCL & KVL	Pragna		04		

Sl. No.	Department	Laboratory Name	Equipment/Facilities Details	Remarks
1	AIML	Data Structure Lab	30 Computer Systems	
2	AIML	Microcontroller Lab	30 Computer Systems, Microcontroller KITS	
3	AIML	Machine Learning Lab	30 Computer Systems	
Sl. No.	Department	Laboratory Name	Equipment/Facilities Details	Remarks
1	ISE	Data Structure Lab	30 Computer Systems	
2	ISE	Microcontroller Lab	30 Computer Systems, Microcontroller KITS	
3	ISE	File Structure Lab	30 Computer Systems	
4	ISE	Software Testing Lab	30 Computer Systems	
5	ISE	Analysis and Design Lab	30 Computer Systems	

6	ISE	Web Programming Lab	30ComputerSystems	
7	ISE	Machine LearningLab	30ComputerSystems	
8	ISE	Mobile Application Development	30ComputerSystems	
	ChemistryLab		Conductivity meter	
			DigitalCond.Meter	
			PHMeter	
			BunsenBurner	
			ElectricWaterBath-6hole	
			Photoelectriccolorimeter	
			Calorimeterset	
			BunsenBurnerwithSC	
			DigitalCond.Meter	
			CentrifugeMechine	
			Vaccumpump	
			Heatingmantle250ml	
			Heatingmantle500ml	
			Heatingmantle1000ml	
			Micromagneticstirrer	
			Waterbathelectrical6holes	
			Singlepanadjustableweight	
		MeltingpointapparatusElec.		

		StopClock ESAL	
		Electricalbalancesingle pan	
		Flaskshaker	
		ThermostatWaterbath	
		Electricalhot plate	
		Hotairoven18"x18"	
		Deionizer50ltrcapacitywithanalog conductivity meter	
		FGL613DigitalPHmeter	
		FGCL157DigitalColorimeter	
		BunsenBurnor,Taps,rubbertube	
		Fireextinguisher(3.2 kg)	
		DigitalPotentiometer	
		FGL16133EDig.PHmeter	
		DigitalFlamePhotometer	
		DigitalConductivity	
		DigitalPHmeter	
		Colorimeter	

●List of Experimental Setup in each Laboratory/Workshop

Sl.No.	Department	Laboratory Name	ListofExperiments	Remarks
1		Electronic Devices and Instrumentation Lab	Electronic Devices and InstrumentationLab(Third semester) PARTA:Experimentsusing	

	ECE		<p>Discrete components</p> <ol style="list-style-type: none"> 1. Conduct experiment to test diode clipping (single/double ended) and clamping circuits (positive/negative). 2. Half wave rectifier and Full wave rectifier with and without filter and measure the ripple factor. 3. Characteristics of Zener diode and design a Simple Zener voltage regulator to determine line and load regulation. 4. Characteristics of LDR and Photodiode and turn on an LED using LDR 5. Static characteristics of SCR. 6. SCR Controlled HWR and FWR using RC triggering circuit 7. Conduct an experiment to measure temperature in terms of current/voltage using a temperature sensor bridge. 8. Measurement of Resistance using Wheatstone and Kelvin's bridge. <p>PART- B: Simulation using EDA software</p> <p>(EDWinXP, PSpice, MultiSim, Proteus, Circuit Lab or any equivalent tool)</p> <ol style="list-style-type: none"> 1. Input and Output characteristics of BJT Common emitter configuration and 	
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			<p>evaluation of parameters.</p> <p>2. Transfer and drain characteristics of a JFET and MOSFET.</p> <p>3. UJT triggering circuit for Controlled Fullwave Rectifier.</p> <p>4. Design and simulation of Regulated power supply.</p>	
2		DSDlab	<p>DSDlab(Third semester)</p> <p>1. Verify</p> <p>(i) Demorgan's Theorem for 2 variables.</p> <p>(ii) The sum-of product and product-of-sum expressions using universal gates. L1, L2, L3</p> <p>2. Design and implement</p> <p>(i) Half Adder & Full Adder using i) basic gates. ii) NAND gates</p> <p>(ii) Half subtractor & Full subtractor using i) basic gates ii) NAND gates L3, L4</p> <p>3. Design and implement</p> <p>(i) 4-bit Parallel Adder/Subtractor using IC 7483.</p> <p>(ii) BCD to Excess-3 code conversion and vice-versa. L3, L4</p> <p>4. Design and Implementation of</p> <p>(i) 1-bit Comparator</p>	

			<p>(ii) 5-bit Magnitude Comparator using IC 7485. L3, L4</p> <p>5. Realize</p> <p>(i) Adder & Subtractor using IC 74153.</p> <p>(ii) 4-variable function using IC 74151 (8:1 MUX). L2, L3, L4</p> <p>6. Realize</p> <p>(i) Adder & Subtractor using IC 74139.</p> <p>(ii) Binary to Gray code conversion & vice-versa (74139) L2, L3, L4</p> <p>7. Realize the following flip-flops using NAND Gates. Master-Slave JK, D & T Flip-Flop. L2, L3</p> <p>8. Realize the following shift registers using IC 7474/7495</p> <p>(i) SISO (ii) SIPO (iii) PISO (iv) PIPO (v) Ring (vi) Johnson counter L2, L3</p> <p>9. Realize (i) Design Mod – N Synchronous Up Counter & Down Counter using 7476 JK Flip-flop</p> <p>(ii) Mod-N Counter using IC 7490/7476</p> <p>(iii) Synchronous counter using IC 74192 L2, L3</p> <p>10. Design Pseudo Random Sequence generator using 7495. L2, L3</p> <p>11. Design Serial Adder with</p>	
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			Accumulator and Simulate using Simulation tool. L2, L3, L4 12. Design Binary Multiplier and Simulate using Simulation tool. L2, L3, L4	
3		VLSI lab	<p>VLSI lab (Seventh semester)</p> <p>Part-A</p> <p>Analog Design</p> <p>Use any VLSI design tool to carry out the experiments, use library files and technology files below 180 nm.</p> <p>1.a) Capture the schematic of CMOS inverter with load capacitance of 0.1 pF and set the width of inverter with $W_n = W_p$, $W_n = 2W_p$, $W_n = W_p/2$ and length at selected technology. Carry out the following:</p> <p>i) Set the input signal to a pulse with rise time, fall time of 1 ns and pulse width of 10 ns and time period of 20 ns and plot the input voltage and output voltage of designed inverter?</p> <p>ii) From the simulation results compute t_{pHL}, t_{pLH} and t_d for all three geometrical settings of width?</p> <p>iii) Tabulate the results of delay and find the best geometry for minimum delay for CMOS inverter?</p> <p>1.b) Draw layout of inverter with</p>	

			<p>Wp/Wn =40/20, use optimum layout methods. Verify for DRC and LVS, extract parasitic and perform post layout simulations, compare the results with pre-layout simulations. Record the observations.</p> <p>2.a) Capture the schematic of 2-input CMOS NAND gate having similar delay as that of CMOS inverter computed in experiment</p> <p>1. Verify the functionality of NAND gate and also find out the delay t_d for all four possible combinations of input vectors. Tabulate the results. Increase the drive strength to 2X and 4X and tabulate the results.</p> <p>2. b) Draw layout of NAND with Wp/Wn=40/20, use optimum layout methods. Verify for DRC and LVS, extract parasitic and perform post layout simulations, compare the results with pre- layout simulations. Record the observations.</p> <p>3. a) Capture schematic of Common Source Amplifier with PMOS Current Mirror Load and find its transient response and AC response? Measure the Unity Gain Bandwidth (UGB), amplification factor by varying transistor geometries, study the impact of variation in width to UGB.</p> <p>3. b) Draw layout of common source amplifier, use optimum layout methods. Verify for DRC and LVS, extract parasitic and</p>	
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			<p>Perform postlayout simulations, compare the results with pre-layout simulations. Record the observations.</p> <p>4.a) Capture schematic of two-stage operational amplifier and measure the following:</p> <ul style="list-style-type: none"> i. UGB ii. dB bandwidth iii. Gain margin and phase margin with and without coupling capacitance iv. Use the op-amp in the inverting and non-inverting configuration and verify its functionality v. Study the UGB, 3dB bandwidth, gain and power requirement in op-amp by varying the stage wise transistor geometries and record the observations. <p>4.b) Draw layout of two-stage operational amplifier with minimum transistor width set to 300 (in 180/90/45 nm technology), choose appropriate transistor geometries as per the results obtained in 4.a. Use optimum layout methods. Verify for DRC and LVS, extract parasitic and perform post layout simulations, compare the results with pre-layout simulations. Record the observations.</p>	
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			<p>Part -</p> <p>BDigital Desig</p> <p>n</p> <p>Carry out the experiments using semicustom design flow or ASIC design flow, use technology library 180/90/45nm and below</p> <p>Note: The experiments can also be carried out using FPGA design flow, it is required to set appropriate constraints in FPGA advanced synthesis options</p> <p>1. Write verilog code for 4-bit up/down asynchronous reset counter and carry out the following:</p> <p>a. Verify the functionality using test bench</p> <p>b. Synthesize the design by setting area and timing constraint. Obtain the gate level netlist, find the critical path and maximum frequency of operation. Record the area requirement in terms of number of cells required and properties of each cell in terms of driving strength, power and area requirement.</p> <p>c. Perform the above for 32-bit up/down counter and identify the critical path, delay of critical path, and maximum frequency of operation, total number of cells required and total area.</p> <p>2. Write verilog code for 4-bit adder and verify its functionality using test bench. Synthesize the design by setting proper</p>	
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			<p>constraints and obtain the netlist. From the report generated identify critical path, maximum delay, total number of cells, power requirement and total area required. Change the constraints and obtain optimum synthesis results.</p> <p>3. Write verilog code for UART and carry out the following:</p> <p>a. Perform functional verification using test bench</p> <p>b. Synthesize the design targeting suitable library and by setting area and timing constraints</p> <p>c. For various constraints set, tabulate the area, power and delay for the synthesized netlist</p> <p>d. Identify the critical path and set the constraints to obtain optimum gate level netlist with suitable constraints</p> <p>4. Write verilog code for 32-bit ALU supporting four logical and four arithmetic operations, use case statement and if statement for ALU behavioral modeling.</p> <p>a. Perform functional verification using test bench</p> <p>b. Synthesize the design targeting suitable library by setting area and timing constraints</p> <p>c. For various constraints set, tabulate the area, power and delay for the synthesized netlist</p>	
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			<p>d Identify the critical path and set the constraints to obtain optimum gate level netlist with suitable constraints. Compare the synthesis results of ALU modeled using IF and CASE statements. 5. Write verilog code for Latch and Flip-flop, Synthesize the design and compare the synthesis report (D, SR, JK).</p> <p>6. For the synthesized netlist carry out the following for any two above experiments:</p> <p>a Floor planning (automatic), identify the placement of pads</p> <p>b. Placement and Routing, record the parameters such as no. of layers used for routing, flip method for placement of standard cells, placement of standard cells, routes of power and ground, and routing of standard cells</p> <p>c. Physical verification and record the LVS and DRC reports</p> <p>d Perform Back annotation and verify the functionality of the design</p> <p>e. Generate GDSII and record the number of masks and its color composition</p>	
4		HDL Lab	<p>Microcontroller lab (Fourth semester)</p> <p>I. PROGRAMMING</p> <p>1. Data Transfer: Block Move,</p>	

			<p>Exchange, Sorting, Finding largest element in an array.</p> <p>2. Arithmetic Instructions - Addition/subtraction, multiplication and division, square, Cube – (16 bits Arithmetic operations – bit addressable).</p> <p>3. Counters.</p> <p>4. Boolean & Logical Instructions (Bit manipulations).</p> <p>5. Conditional CALL & RETURN.</p> <p>6. Code conversion: BCD – ASCII; ASCII – Decimal; Decimal – ASCII; HEX-Decimal and Decimal-HEX.</p> <p>7. Program to generate delay, Programs using serial port and on-Chip timer/counter.</p> <p>II. INTERFACING</p> <p>1. Interface a simple toggle switch to 8051 and write an ALP to generate an interrupt which switches on an LED</p> <p>(i) continuously as long as switch is on and</p> <p>(ii) only once for a small time when the switch is turned on.</p> <p>2. Write a C program to</p> <p>(i) transmit and</p> <p>(ii) to receive a set of characters serially by interfacing 8051 to a terminal.</p>	
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			<p>3. Write ALPs to generate waveforms using ADC interface.</p> <p>4. Write ALP to interface an LCD display and to display a message on it.</p> <p>5. Write ALP to interface a Stepper Motor to 8051 to rotate the motor.</p> <p>6. Write ALP to interface ADC-0804 and convert an analog input connected to it.</p>	
			<p>HDL Lab (Fifth semester)</p> <p>PART A: Programming</p> <p>1. Write Verilog program for the following combinational design along with test bench to verify the design:</p> <ol style="list-style-type: none"> 2 to 4 decoder realization using NAND gates only (structural model) 8 to 3 encoder with priority and without priority (behavioural model) 8 to 1 multiplexer using case statement and if statements 4-bit binary to gray converter using 1-bit gray to binary converter, 1-bit adder and subtractor <p>2. Model in Verilog for a full adder and add functionality to perform logical operations of XOR, XNOR, AND and OR gates. Write test bench with appropriate input patterns to verify the modeled behavior.</p> <p>3. Verilog 32-bit ALU shown in figure below and verify the functionality of ALU by selecting appropriate test patterns. The functionality of the ALU is presented in Table 1.</p> <ol style="list-style-type: none"> Write test bench to verify the functionality of the ALU considering all possible input patterns The enable signal will set the output to required functions if enabled, if disabled all the outputs are set to tri-state The acknowledge signal is set high after every operation is completed 	

			<p>Result[32:0]</p> <p>4. Write Verilog code for SR, D and JK and verify the flip flop.</p> <p>5. Write Verilog code for 4-bit BCD synchronous counter.</p> <p>6. Write Verilog code for counter with given input clock and check whether it works as clock divider performing division of clock by 2, 4, 8 and 16. Verify the functionality of the code.</p> <p><u>PART-B: Interfacing and Debugging</u> (EDWinXP, PSpice, MultiSim, Proteus, CircuitLab or any other equivalent tool can be used)</p> <p>1. Write a Verilog code to design a clock divider circuit that generates 1/2, 1/3rd and 1/4th clock from a given input clock. Port the design to FPGA and validate the functionality through oscilloscope.</p> <p>2. Interface a DC motor to FPGA and write Verilog code to change its speed and direction.</p> <p>3. Interface a Stepper motor to FPGA and write Verilog code to control the Stepper motor rotation which in turn may control a Robotic Arm. External switches to be used for different controls like rotate the Stepper motor (i) +N steps if Switch no.1 of a Dip switch is closed (ii) +N/2 steps if Switch no. 2 of a Dip switch is closed (iii) -N steps if Switch no. 3 of a Dip switch is closed etc.</p>	
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			<p>4. Interface a DAC to FPGA and write Verilog code to generate Sine wave of frequency F KHz (eg. 200 KHz) frequency. Modify the code to down sample the frequency to F/2 KHz. Display the Original and Down sampled signals by connecting them to an oscilloscope.</p> <p>5. Write Verilog code using FSM to simulate elevator operation.</p> <p>6. Write Verilog code to convert an analog input of a sensor to digital form and to display the same on a suitable display like set of simple LEDs, 7-segment display digit or LCD display.</p>	
			<p>Embedded Systems lab (Sixth semester)</p> <p>PART A:</p> <ol style="list-style-type: none"> 1. ALP to multiply two 16 bit binary numbers. 2. ALP to find the sum of first 10 integer numbers. 3. ALP to find the number of 0's and 1's in a 32 bit data 4. ALP to find determine whether the given 16 bit is even or odd 5. ALP to write data to RAM <p>PART B:</p> <ol style="list-style-type: none"> 6. Display "Hello world" message using internal UART 7. Interface and Control the speed 	

			<p>of a DC Motor.</p> <p>8. Interface a Stepper motor and rotate it in clockwise and anti-clockwise direction.</p> <p>9. Interface a DAC and generate Triangular and Square waveforms.</p> <p>10. Interface a 4x4 keyboard and display the key code on an LCD.</p> <p>11. Demonstrate the use of an external interrupt to toggle an LED On/Off.</p> <p>12. Display the Hex digits 0 to F on a 7-segment LED interface, with an appropriate delay.</p> <p>13. Measure Ambient temperature using a sensor and SPI ADC IC.</p>	
5		Advanced Communication lab	<p>Analog circuits lab (Fourth semester)</p> <p>PART A: Hardware Experiments</p> <p>1. Design and set up the Common Source JFET/MOSFET amplifier and plot the frequency response.</p> <p>2. Design and set up the BJT common emitter voltage amplifier with and without feedback and determine the gain-bandwidth product, input and output impedances.</p> <p>3. Design and set-up BJT/FET i) Colpitts Oscillator, and ii) Crystal Oscillator</p>	

			<p>4. Design active second order Butterworth low pass and high pass filters.</p> <p>5. Design Adder, Integrator and Differentiator circuits using Op-Amp</p> <p>6. Test a comparator circuit and design a Schmitt trigger for the given UTP and LTP values and obtain the hysteresis.</p> <p>7. Design 4-bit R-2R Op-Amp Digital to Analog Converter (i) using 4-bit binary input from toggle switches and (ii) by generating digital inputs using mod-16 counter.</p> <p>8. Design Monostable and a stable Multivibrator using 555 Timer.</p> <p>PART-B: Simulation using EDA software (EDWinXP, PSpice, MultiSim, Proteus, CircuitLab or any other equivalent tool can be used)</p> <p>1. RC Phase shift oscillator and Hartley oscillator</p> <p>2. Narrow Band-pass Filter and Narrow band-reject filter</p> <p>3. Precision Half and full wave rectifier</p> <p>4. Monostable and A stable Multivibrator using 555 Timer.</p>	
Communication lab (Sixth semester)			PART-A: Expt. 1 to Expt. 5 have to	

			<p>beperformed using discrete components.</p> <ol style="list-style-type: none"> 1. Amplitude Modulation and Demodulation: i) Standard AM, ii) DSBSC (LM741 and LF398 ICs can be used) 2. Frequency modulation and demodulation (IC8038/2206 can be used) 3. Pulse sampling, flat top sampling and reconstruction 4. Time Division Multiplexing and Demultiplexing of two band limited signals. 5. FSK and PSK generation and detection 6. Measurement of frequency, guide wavelength, power, VSWR and attenuation in microwave test bench. 7. Obtain the Radiation Pattern and Measurement of directivity and gain of microstrip dipole and Yagi antennas. 8. Determination of <ol style="list-style-type: none"> a. Coupling and isolation characteristics of microstrip directional coupler. b. Resonance characteristics of microstrip ring resonator and computation of dielectric constant of the substrate. c. Power division and isolation of 	
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			<p>microstrippowerdivider.</p> <p>PART-B: Simulation Experiments using SCILAB/MATLAB/Simulinkor LabVIEW</p> <p>1. To Simulate NRZ, RZ, half-sinusoid&raisedcosinepulses and generate eye diagram for binary polar signaling.</p> <p>2Pulsecodemodulationand demodulation system.</p> <p>3. ComputationsoftheProbability ofbit error for coherent binary ASK, FSK and PSK for an AWGN Channel and compare them with their performance curves.</p> <p>4. DigitalModulationSchemesi) DPSK Transmitter and Receiver, ii)QPSKTransmitterandReceiver.</p>	
6		DSPlab	<p>DSP lab(Fifthsemester)</p> <p>FollowingExperimentstobedone using MATLAB/ SCILAB/OCTAVE orequivalent:</p> <p>1. Verification of sampling theorem(useinterpolation function).</p> <p>2Linearandcircularconvolution of two given sequences, Commutative, distributive and associative property of convolution.</p> <p>3. Auto and cross correlation of twosequencesandverificationof their properties</p> <p>4. Solvingagivendifference</p>	

			<p>equation.</p> <p>5. Computation of N-point DFT of a given sequence and to plot magnitude and phase spectrum (using DFT equation and verify it by built-in routine).</p> <p>6. (i) Verification of DFT properties (like Linearity and Parseval's theorem, etc.) (ii) DFT computation of square pulse and Sinc function etc.</p> <p>7. Design and implementation of Lowpass and High pass FIR filter to meet the desired specifications (using different window techniques) and test the filter with an audio file. Plot the spectrum of audio signal before and after filtering.</p> <p>8. Design and implementation of a digital IIR filter (Low pass and High pass) to meet given specifications and test with an audio file. Plot the spectrum of audio signal before and after filtering.</p> <p>Following Experiments to be done using DSP kit</p> <p>9. Obtain the Linear convolution of two sequences.</p> <p>10. Compute Circular convolution of two sequences.</p> <p>11. Compute the N-point DFT of a given sequence.</p> <p>12. Determine the Impulse response of first order and second</p>	
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			<p>ordersystem.</p> <p>13.Generationofsinewaveand standard test signals</p>	
			<p>CCNlab(Seventhsemester)</p> <p>PART-A: Simulation experimentsusingNS2/NS3/ OPNET/ NCTUNS/ NetSim/QuaiNetoranyother equivalent tool</p> <p>1. Implement a point to point network with four nodes and duplex links between them. Analyzethenetworkperformance by setting the queue size and varying the bandwidth.</p> <p>2. Implementafournodepointto point network with links n0-n2, n1-n2andn2-n3.ApplyTCPagent betweenenn0-n3andUDPbetween n1-n3.Applyrelevantapplications over TCP and UDP agents changing the parameter and determinethenumberofpackets sent by TCP/UDP.</p> <p>3. ImplementEthernetLANusing n (6-10) nodes. Compare the throughputbychangingtheerror rate and data rate.</p> <p>4. ImplementEthernetLANusing n nodes and assign multiple traffic to the nodes and obtain congestion window for different sources/ destinations.</p> <p>5. Implement ESS with transmission nodes in Wireless LANandobtaintheperformance parameters.</p>	

			<p>6.ImplementationofLinkstate routing algorithm.</p> <p>PART-B:Implementthe following in C/C++</p> <p>1. Write a program for a HDLC frametoperformthefollowing.</p> <p>i)Bitstuffing</p> <p>ii)Characterstuffing.</p> <p>2 Write a program for distance vectoralgorithmtofindsuitable path for transmission.</p> <p>3. ImplementDijkstra'salgorithm to compute the shortest routing path.</p> <p>4. For the given data, use CRC-CCITTpolynomialtoobtainCRC code.Verifytheprogramforthe cases</p> <p>a)Without error</p> <p>b)Witherror</p> <p>5. Implementation of Stop and WaitProtocolandSlidingWindow Protocol</p> <p>6. Writeaprogramforcongestion controlusingleakybucket algorithm</p>	
7		Electrical lab	<p>Basic Electrical Engineering lab(Firstandsecondsemester)</p> <p>1 VerificationofKCLandKVLfor DC circuits</p> <p>2 Verificationofmaximumpower</p>	

			<p>theorem.</p> <p>3 Measurement of Current, Power, and Power Factor of Incandescent Lamp, Fluorescent Lamp and LED Lamp.</p> <p>4 Measurement of Resistance and Inductance of a Choke coil using three voltmeter method.</p> <p>5 Determination of Phase and Line quantities in three-phase star and delta connected loads.</p> <p>6 Measurement of 3-phase Power using Two Wattmeter Method.</p> <p>7 Determination of efficiency of a single-phase transformer by direct load test.</p> <p>8 Two Way and Three-Way Control of Lamp and Formation of Truth Table.</p> <p>9 Measurement of Earth Resistance</p> <p>10 Study of the effect of Open and Short circuits in simple circuits</p>	
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Sl. No.	Department	Laboratory Name	List of Experiments	Remarks
	CIVIL ENGINEERING	COMPUTER AIDED BUILDING PLANNING AND DRAWING	<p>Drawings Related to Different Building Elements:</p> <p>Following drawings are to be prepared for the data given using CAD Software</p> <p>a) Cross section of Foundation, masonry wall, RCC columns with isolated & combined footings.</p> <p>b) Different types of bonds in brick</p>	

			<p>masonry.</p> <p>c) Different types of staircases – Dog legged, Open well.</p> <p>d) Lintel and chajja.</p> <p>e) RCC slabs and beams.</p> <p>f) Cross section of pavement.</p> <p>g) Septic Tank and sedimentation Tank.</p> <p>h) Layout plan of Rainwater recharging and harvesting system.</p> <p>i) Cross sectional details of a road for a Residential area with provision for all services.</p> <p>j) Steel truss (connections Bolted).</p> <p>Building Drawings: Principles of planning, Planning regulations and building bye-laws, factors affecting site selection, Functional planning of residential and public buildings, design aspects for different public buildings. Recommendations of NBC.</p> <p>Drawing of Plan, elevation and sectional elevation including electrical, plumbing and sanitary services using CAD software for:</p> <ol style="list-style-type: none"> 1. Single and double story residential building. 2. Hostel building. 3. Hospital building. 4. School building. 	
		<p>BUILDING MATERIALS TESTING LABORATORY</p>	<ol style="list-style-type: none"> 1. Tension test on mild steel and HYSD bars. 2. Compression test on mild steel, cast iron and wood. 3. Torsion test on mild steel circular sections. 4. Bending Test on Wood Under two point loading. 5. Shear Test on Mild steel - single and double shear. 6. Impact test on Mild Steel (Charpy & Izod). 7. Hardness test on ferrous and non-ferrous metals - Brinell's, Rockwell 	

			<p>and Vicker's.</p> <p>8. Tests on Bricks, Tiles and Concrete Blocks.</p> <p>9. Tests on Fine aggregates-Moisture content, Specific gravity, Bulk density, Sieve analysis and Bulking.</p> <p>10. Tests on Coarse aggregates-Absorption, Moisture content, specific gravity, Bulk density and Sieve analysis.</p> <p>11. Demonstration of Strain gauges and Strain indicators.</p>	
		<p>ENGINEERING GEOLOGY LABORATORY</p>	<p>1. Physical properties of minerals: Identification of</p> <p>i. Rock Forming minerals - Quartz group, Feldspar group, Garnet group, Mica group & Talc, Chlorite, Olivine, Asbestos, Calcite, Gypsum, etc</p> <p>ii. Ore forming minerals- Magnetite, Hematite, Pyrite, Pyralusite, Graphite, Chromite, etc</p> <p>2. Engineering Properties of Rocks: Identification of</p> <p>i. Igneous rocks- Types of Granites, Dolerite, Granite Porphyry, Basalt, Pumice etc</p> <p>ii. Sedimentary rocks- Sandstone, Limestone, Shale, Laterite, Breccia etc</p> <p>iii. Metamorphic rocks- Gneiss, Slate, Schist, Marble, Quartzite etc</p> <p>3. Borehole problems: Determination of subsurface behavior of rocks, their attitude related to foundation, tunnels, reservoirs and mining. Triangular and Square methods. (2 methods)</p> <p>4. Dip and Strike problems. Determine Apparent dip and True dip. (2 methods)</p> <p>5. Calculation of Vertical, True thickness and width of the outcrops. (3 methods)</p> <p>6. Study of Toposheets and Interpretation, Extraction of Drainage</p>	

			<p>Basin and its Morphometric Analysis. (3 Toposheets)</p> <p>7. Interpretation and drawing of sections for geological maps showing tilted beds, faults, unconformities etc. (10 Maps)</p> <p>8. Interpretation of Satellite Images. (2 Satellite images)</p> <p>9. Field work – To identify Minerals, Rocks, Geomorphology and Structural features with related to the Civil Engineering projects.</p>	
		<p>FLUID MECHANICS AND HYDRAULIC MACHINES LABORATORY</p>	<ol style="list-style-type: none"> 1. Verification of Bernoulli's equation. 2. Determination of Cd for Venturimeter and Orificemeter. 3. Determination of hydraulic coefficient of small vertical orifice. 4. Determination of Cd for Rectangular and Triangular notch 5. Determination of Cd for Ogee and Broad crested weir 6. Determination of Cd for Venturiflume 7. Determination of force exerted by a jet on flat and curved vanes. 8. Determination of efficiency of Pelton wheel turbine 9. Determination of efficiency of Francis turbine 10. Determination of efficiency of Kaplan turbine 11. Determination of efficiency of centrifugal pump 12. Determination of Major Loss in Pipes 13. Determination of Minor losses in pipe due to sudden enlargement, sudden contraction and bend. 	
		<p>SURVEYING PRACTICE</p>	<ol style="list-style-type: none"> 1. a) Measurements of distances using tape along with horizontal planes and slopes, direct ranging. b) Setting out perpendiculars. Use of cross staff, optical square. 2. Measurements of bearings / directions using prismatic compass, 	

			<p>setting of geometrical figures using prismatic compass.</p> <p>3. Determination of distance between two inaccessible points using compass</p> <p>4. Determination of reduced level of points using dumpy level/auto level (simple leveling)</p> <p>5. Determination of reduced level of points using dumpy level/auto level (differential leveling and inverted leveling).</p> <p>6. To determine the difference in elevation between two points using Reciprocal leveling and to determine the collimation error.</p> <p>7. To conduct profile leveling, cross sectioning and block leveling. Plotting profile and cross sectioning in excel. Block contour on graph paper to scale.</p> <p>8. Measurement of horizontal angle by repetition and reiteration methods and Measurement of vertical angles using theodolite.</p> <p>9. Determination of horizontal distance and vertical height to a base in accessible object using theodolite by single plane and double plane method.</p> <p>10. To determine distance and elevation using tachometric surveying with horizontal and inclined line of sight.</p> <p>11. Closed traverse surveying using Theodolite and applying corrections for error of closure by transit rule and Bowditch rule.</p> <p>12. To locate the points using Radiation and Intersection method of Plane table surveying.</p> <p>13. To solve three point problem in plane table using Bessel's graphical solution.</p> <p>14. Demonstration of Minor instruments like Clinometer, Ceylon Ghat tracer, Box sextant, Hand level, Planimeter, nautical sextant and Penta</p>	
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			graph.	
		<p>CONCRETE AND HIGHWAY MATERIALS LABORATORY</p>	<p>Part A: Concrete Lab</p> <ol style="list-style-type: none"> 1. TestsonCement: <ol style="list-style-type: none"> a. NormalConsistency b. Settingtime c. Compressivestrength d. finenessbyairpermeabilitytest e. specificgravity 2. TestsonConcrete: <ol style="list-style-type: none"> a. DesignofconcretemixasperIS-10262 b. Testsonfreshconcrete: <ol style="list-style-type: none"> i. slump, ii. compactionfactorand iii. VeeBeetest c. Tests onhardenedconcrete: <ol style="list-style-type: none"> i. compressivestrengthtest, ii. splittensilestrengthtest, iii. flexuralstrengthtest d. NDTtestsbyreboundhammerand pulse velocity test. 3. TestsonSelfCompactingConcrete: <ol style="list-style-type: none"> a. Designofselfcompactingconcrete, Asper Is10262:2019 b. slumpflowtest, c. V-funneltest, d. J-Ringtest, e. UBoxtestand f. LBoxtest <p>Part B: HighwaymaterialsLab</p> <ol style="list-style-type: none"> 1. TestsonAggregates <ol style="list-style-type: none"> a. AggregateCrushingvalue b. LosAngelesabrasiontest c. Aggregateimpacttest d. Aggregateshapetests(combined index and angularity number) 2. TestsonBituminousMaterials <ol style="list-style-type: none"> a. Penetrationtest b. Ductility test c. Softeningpointtest d. Specificgravitytest e. Viscositytest bytarviscometer f. BituminousMixDesignbyMarshal Method (Demonstration only) 3. TestsonSoil 	

			<ul style="list-style-type: none"> a. Wet sieve analysis b. CBR test 	
		<p>SOFTWARE APPLICATION LABORATORY</p>	<p>Module-1</p> <p>Use of civil engineering software's:</p> <p>Use of software's for:</p> <ul style="list-style-type: none"> 1. Analysis of plane trusses, continuous beams, portal frames. 2. 3D analysis of multi-storied frame structures. <p>Module-2</p> <ul style="list-style-type: none"> 1. Project Management-Exercise on Project planning and scheduling of a building project using any project management software: <ul style="list-style-type: none"> a. Understanding basic features of Project management software b. Constructing Project: create WBS, Activities, and tasks and Computation Time using Excel spread sheet and transferring the same to Project management software. c. Identification of Predecessor and Successor activities with constrain d. Constructing Network diagram (AON Diagram) and analyzing for Critical path, Critical activities and Other non Critical paths, Project duration, Floats. e. Study on various View options available f. Basic understanding about Resource Creation and allocation g. Understanding about Splitting the activity, Linking multiple activity, assigning Constrains, Merging Multiple projects, Creating Baseline Project 1. GIS Applications using open source software: <ul style="list-style-type: none"> a. To create shape files for point, line and polygon features with a map as reference. b. To create decision maps for specific purpose. <p>Module-3</p>	

			Use of EXCEL spreadsheets: Design of singly reinforced and doubly reinforced rectangular beams, design of one way and two way slabs, computation of earthwork, Design of horizontal curve by offset method, Design of superelevation.	
		ENVIRONMENTAL ENGINEERING LABORATORY	<ol style="list-style-type: none"> 1. Preparation chemical solutions required for analysis and sampling methodologies 2. Determination of pH, Conductivity, TDS and Turbidity. 3. Determination of Acidity and Alkalinity 4. Determination of Calcium, Magnesium and Total Hardness. 5. Determination of Dissolved Oxygen 6. Determination of BOD. 7. Determination of Chlorides 8. Determination of percentage of % of available chlorine in bleaching powder sample, Determination of Residual Chlorine and chlorine demand. 9. Determination of Solids in Sewage: <ol style="list-style-type: none"> i) Total Solids, ii) Suspended Solids, iii) Dissolved Solids, iv) Volatile Solids, Fixed Solids v) Settleable Solids. 10. Determination of optimum coagulant dosage using Jar test apparatus. 11. Determination of Nitrates and Iron by spectrophotometer 12. Determination of COD (Demonstration) 13. Air Quality Monitoring (Demonstration) 14. Determination of Sound by Sound level meter at different locations (Demonstration) 	
		COMPUTER AIDED DETAILING OF STRUCTURES	<p>Module-1 Detailing of RCC Structures</p> <ul style="list-style-type: none"> · Beams – Simply supported, Cantilever and Continuous. 	

			<ul style="list-style-type: none"> · Slab–Oneway, Twoway and One-way continuous. · Staircase–Doglegged · Cantilever Retaining wall · Counter Fort Retaining wall · Circular Water Tank, Rectangular Water Tank. <p>Module-2 Detailing of Steel Structures</p> <ol style="list-style-type: none"> 1. Connections–Beam to beam, Beam to Column by Bolted and Welded Connections. 2. Built-up Columns with lacings and battens 3. Column bases and Gusseted bases with bolted and welded connections. 4. Roof Truss–Welded and Bolted 5. Welded Plate girder 6. Gantry Girder 	
		GEOTECHNICAL ENGINEERING LABORATORY	<ol style="list-style-type: none"> 1. Field identification of soil, Specific gravity test (pycnometer and density bottle method). Water content determination by oven drying and Pycnometer method, rapid moisture meter method. 2. Grain size analysis <ol style="list-style-type: none"> i. Sieve analysis ii. Hydrometer analysis 3. In-situ density tests <ol style="list-style-type: none"> i. Core-cutter method ii. Sand replacement method 4. Consistency limits <ol style="list-style-type: none"> i. Liquid limit test (by Casagrande's sand cone penetration method) ii. Plastic limit test iii. Shrinkage limit test 5. Standard compaction test (light and heavy compaction) 6. Co-efficient of permeability test <ol style="list-style-type: none"> i. Constant head test ii. Variable head test 7. Shear strength tests <ol style="list-style-type: none"> i. Unconfined compression test ii. Direct shear test iii. Triaxial test (unconsolidated 	

			<p>undrained test only)</p> <p>8. Consolidation test: To determine pre consolidation pressure only (half an hour per loading-test).</p> <p>9. Laboratory vane shear test</p> <p>10. Demonstration of Swell pressure test, Standard penetration test and boring equipment</p>	
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Sl. No.	Department	Laboratory Name	List of Experiments	Remarks
	Mechanical Engineering	Material Testing lab	<p>1 Preparation of specimen for Metallographic examination of different engineering materials.</p> <p>To report microstructures of plain carbon steel, tool steel, gray C.I, SG iron, Brass, Bronze & composites.</p> <p>2 Heat treatment: Annealing, normalizing, hardening and tempering of steel.</p> <p>Metallographic specimens of heat treated components to be supplied and students should report microstructures of furnace cooled, water cooled, air cooled, tempered steel. Students should be able to distinguish the phase changes in a heat treated specimen compared to untreated specimen.</p> <p>3 Brinell, Rockwell and Vickers's Hardness tests on untreated and heat treated specimens.</p> <p>4 To study the defects of Cast and Welded components using Non-destructive tests like:</p> <p>a) Ultrasonic flaw detection</p> <p>b) Magnetic crack detection</p> <p>c) Dye penetration testing.</p>	

			<p>PART B</p> <p>5 Tensile, shear and compression tests of steel, aluminum and cast iron specimens using Universal Testing Machine</p> <p>6 Torsion Test on steel bar.</p> <p>7 Bending Test on steel and wood specimens.</p> <p>8 Izod and Charpy Test on Mild steel and C.I Specimen.</p> <p>9 To study the wear characteristics of ferrous and non-ferrous materials under different parameters.</p> <p>10 Tensile, shear and compression tests of steel, aluminum and cast iron specimens using Universal Testing Machine</p> <p>11 Fatigue Test (demonstration only)</p>	
		<p>Mechanical Measurements and Metrology lab</p>	<p>1 Calibration of Pressure Gauge</p> <p>2 Calibration of Thermocouple</p> <p>3 Calibration of LVDT</p> <p>4 Calibration of Load cell</p> <p>5 Determination of modulus of elasticity of a mild steel specimen using strain gauges.</p> <p>PART B</p> <p>6 Measurements using Optical Projector/Tool makers' Microscope.</p> <p>7 Measurement of angle using Sine Centre/Sine bar / bevel protractor</p>	

			<p>8 Measurement of alignment using Autocollimator / Roller set</p> <p>9 Measurement of cutting tool forces using: Lathe tool Dynamometer</p> <p>10 Measurement of Screw thread parameters using two wire or three -wire methods.</p> <p>11 Measurement of surface roughness using Tally Surf/Mechanical Comparator</p> <p>12 Measurement of gear tooth profile using gear tooth Vernier/Gear tooth micrometer</p> <p>13 Calibration of Micrometer using slip gauges</p> <p>14 Measurement using Optical Flats</p>	
		<p>Workshop and Machine Shop Practice (Consists of Fitting, and Machining)</p>	<p>1 Preparation of at least two fitting joint models by proficient handling and application of hand tools- V block, marking gauge, files, hacksaw drill setc. PART B</p> <p>2 Preparation of three models on lathe involving- Plain turning, Taper turning, Step turning, Thread cutting, Facing, Knurling, Drilling, Boring, Internal Thread cutting and Eccentric turning. Exercises should include selection of cutting parameters and cutting time estimation.</p> <p>PART C</p>	

			<p>3 Cutting of V Groove/dovetail/Rectangular groove using a shaper.</p> <p>Cutting of Gear Teeth using Milling Machine.</p> <p>Exercises should include selection of cutting parameters and cutting time estimation.</p>	
	<p>Foundry, Forging and Welding lab</p>	<p>PART A</p> <p>Testing of Molding sand and Core sand.</p> <p>Preparation of sand specimens and conduction of the following tests:</p> <ol style="list-style-type: none"> 1. Compression, Shear and Tensile test on Universal Sand Testing Machine. 2. Permeability test 3. Sieve Analysis to find Grain Fineness Number (GFN) of Base Sand 4. Clay content determination on Base Sand. <p>Welding Practice:</p> <p>Use of Arc welding tools and welding equipment</p> <p>Preparation of welded joints using Arc Welding equipment</p> <p>L-Joint, T-Joint, Butt joint, V-Joint, Lap joints on M.S. flats</p> <p>PART B</p> <p>Foundry Practice:</p> <p>Use of foundry tools and other equipment for</p>		

			<p>Preparation of moldings and mixture.</p> <p>Preparation of green sand molds kept ready for pouring in the following cases:</p> <ol style="list-style-type: none"> 1. Using two molding boxes (handcut molds). 2. Using patterns (Single piece pattern and Split pattern). 3. Incorporating core in the mold. (Core boxes). 4. Preparation of one casting (Aluminium or cast iron - Demonstration only) <p>PART C</p> <p>Forging Operations: Use of forging tools and other forging equipment.</p> <ul style="list-style-type: none"> • Calculation of length of the raw material required to prepare the model considering scale loss. • Preparing minimum three forged models involving upsetting, drawing and bending operations. 	
		<p>FLUID MECHANICS AND MACHINES LAB</p>	<p>PART A</p> <ol style="list-style-type: none"> 1 Lab layout, calibration of instruments and standards to be discussed 2 Determination of coefficient of friction of flow in a pipe. 3 Determination of minor losses in flow through pipes. 4 Application of momentum equation for determination of coefficient of impact of jet on 	

			<p>flat and curved blades</p> <p>5 Calibration of flow measuring devices. PART B</p> <p>6 Performance on hydraulic Turbines a. Pelton wheel b. Francis Turbine c. Kaplan Turbines</p> <p>7 Performance hydraulic Pumps Single stage and Multi stage centrifugal pumps e. Reciprocating pump.</p> <p>8 Performance test on at two stage Reciprocating Air Compressor.</p> <p>9 Performance test on an Air Blower.</p> <p>PART C (OPTIONAL)</p> <p>10 Visit to Hydraulic Power station/Municipal Water Pump House and Case Studies</p> <p>11 Demonstration of cut section models of Hydraulic turbines and Pumps.</p>	
	ENERGY CONVERSION LABORATORY		<p>PART A</p> <p>1 Lab layout, calibration of instruments and standards to be discussed</p> <p>2 Determination of Flash point and Fire point of lubricating oil using Abel Pensky and Marten's (closed) / Cleveland's (Open Cup) Apparatus.</p> <p>3 Determination of Calorific value of solid, liquid and gaseous fuels.</p> <p>4 Determination of Viscosity of lubricating oil using Redwoods, Saybolt and Torsion</p>	

			<p>Viscometers.</p> <p>5 Valve Timing/port opening diagram of an I.C. Engine.</p> <p>PART B</p> <p>6 Performance Tests on I.C. Engines, Calculations of IP, BP, Thermal efficiency, Volumetric efficiency, Mechanical efficiency, SFC, FP, A:F Ratio, heat balance sheet for</p> <p>a. Four stroke Diesel Engine</p> <p>b. Four stroke Petrol Engine</p> <p>c. Multi Cylinder Diesel/Petrol Engine, (Morse test)</p> <p>d. Two stroke Petrol Engine</p> <p>Variable Compression Ratio I. C. Engine.</p> <p>7 Measurements of Exhaust Emissions of Petrol engine.</p> <p>8 Measurements of Exhaust Emissions of Diesel engine.</p> <p>PART C (OPTIONAL)</p> <p>9 Visit to Automobile Industry/service stations.</p> <p>10 Demonstration of $p\theta$, pV plots using Computerized IC engine test rig</p>	
		COMPUTER AIDED	<p>PART A</p> <p>1 Study of a FEA package and modeling and</p>	

		<p>MODELLING AND ANALYSIS LAB</p>	<p>Stress analysis of:</p> <ul style="list-style-type: none"> a. Bars of constant cross section area, tapered cross section area and stepped bar b. Trusses—(Minimum 2 exercises of different types) c. Beams—Simply supported, cantilever, beams with point load, UDL, beams with varying load etc. (Minimum 6 exercises) d. Stress analysis of a rectangular plate with a circular hole. <p>PART B</p> <p>2 Thermal Analysis—1D & 2D problem with conduction and convection boundary conditions (Minimum 4 exercises of different types) 3</p> <p>Dynamic Analysis to find:</p> <ul style="list-style-type: none"> a) Natural frequency of beam with fixed–fixed end condition b) Response of beam with fixed–fixed end conditions subjected to forcing function c) Response of Bars subjected to forcing functions <p>PART C (only for demo)</p> <p>4a. Demonstrate the use of graphics standards (IGES, STEP etc) to import the model from modeler</p> <p>To solve.</p>	
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			<p>b. Demonstrate one example of contact analysis to learn the procedure to carry out contact analysis.</p> <p>Demonstrate at least two different types of example to model and analyze bars or plates made from composite material.</p>	
		HEAT TRANSFER LAB	<p>PART A</p> <p>1 Determination of Thermal Conductivity of a Metal Rod.</p> <p>2 Determination of Overall Heat Transfer Coefficient of a Composite wall.</p> <p>3 Determination of Effectiveness on a Metallic fin.</p> <p>4 Determination of Heat Transfer Coefficient in free Convection</p> <p>5 Determination of Heat Transfer Coefficient in a Forced Convection</p> <p>6 Determination of Emissivity of a Surface.</p> <p>PART B</p> <p>7 Determination of Stefan Boltzmann Constant.</p> <p>8 Determination of LMDT and Effectiveness in a Parallel Flow and Counter Flow Heat Exchangers.</p> <p>9 Experiments on Boiling of Liquid and Condensation of Vapour.</p>	

			<p>10 Performance Test on a Vapour Compression Refrigeration.</p> <p>11 Performance Test on a Vapour Compression Air – Conditioner.</p> <p>12 Experiment on Transient Conduction Heat Transfer.</p> <p>PART C(OPTIONAL)</p> <p>13 Analysis of steady and transient heat conduction, temperature distribution of plane wall and cylinder using Numerical approach (ANSYS/CFD package).</p> <p>14 Determination of temperature distribution along a rectangular and circular fins subjected to heat loss through convection using Numerical approach (ANSYS/CFD package).</p>	
		<p>COMPUTER AIDED MANUFACTURING LAB</p>	<p>PART - A</p> <p>1. Manual CNC part programming using ISO Format G/M codes for 2 turning and 2 milling parts. Selection and assignment of tools, correction of syntax and logical errors, and verification of tool path using CNC program verification software. PART - B</p> <p>2. CNC part programming using CAM packages.</p>	

	DESIGN LAB	<p>PART - A</p> <p>1 Determination of natural frequency, logarithmic decrement, damping ratio and damping coefficient in a Single degree of freedom vibrating systems (longitudinal and torsional).</p> <p>2 Balancing of rotating masses</p> <p>3 Determination of critical speed of a rotating shaft</p> <p>4 Determination of equilibrium speed, sensitiveness, power and effort of Porter/Proell/Hartnell Governor.</p> <p>PART - B</p> <p>5 Determination of Fringe constant of Photo-elastic material using.</p> <p>a) Circular disc subjected to diametral compression.</p> <p>b) Pure bending specimen (four-point bending).</p> <p>6 Determination of stress concentration using Photo-elasticity for simple components like plate with a hole under tension or bending, circular disk with circular hole under compression, 2D Crane hook</p> <p>7 Determination of Pressure distribution in Journal bearing</p> <p>8 Determination of Principal Stresses and strains in a member subjected to combined loading</p>	
		<p>Simulation of Turning, Drilling, Milling operations.</p> <p>3 typical simulations to be carried out using simulation packages like: CademCAMLab-Pro, Master CAM. Program generation using software. Optimize spindle power, torque utilization, and cycle time. Generation and printing of shop</p>	

			Using Strain settes. 9DeterminationofstressesinCurvedbeam using strain gauge	
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Sl. No.	Department	Laboratory Name	List of Experiments	Remarks
1	AIML	DataStructure Lab	https://vtu.ac.in/pdf/2018syll/is.pdf	
2	AIML	Microcontroller Lab		
3	AIML	Mobile Application Development		
4	CSE		<p>2018 scheme- https://vtu.ac.in/pdf/2018syll/cs.pdf</p> <p>2021 scheme- https://vtu.ac.in/pdf/2021syll/csesch.pdf</p> <p>2022 scheme- https://vtu.ac.in/en/b-e-scheme-syllabus/#menu07</p> <p>2025 scheme- https://vtu.ac.in/en/b-e-scheme-syllabus/</p>	
Sl. No.	Department	Laboratory Name	List of Experiments	Remarks
1	ISE	DataStructure Lab	https://vtu.ac.in/pdf/2018syll/is.pdf	
2	ISE	Microcontroller Lab		
3	ISE	FileStructure Lab		
4	ISE	Software TestingLab		
5	ISE	Analysisand Design Lab		
6	ISE	Web Programming Lab		
7	ISE	Machine LearningLab		

8	ISE	Mobile Application Development		
Sl. No.	Department	Laboratory Name	List of Experiments	Remarks
1	ISE	DataStructure Lab	https://vtu.ac.in/pdf/2018syll/is.pdf	
2	ISE	Microcontroller Lab		
3	ISE	FileStructure Lab		
4	ISE	Software TestingLab		
5	ISE	Analysisand Design Lab		
6	ISE	Web Programming Lab		
7	ISE	Machine LearningLab		
8	ISE	Mobile Application Development		

15.2 Computing Facilities

- Internet Bandwidth: 300MBPS
- Number and configuration of System:
- Total number of systems connected by LAN: 466
- Total number of system connected by WAN: 466
- Major software packages available: Windows 7 Professional 32/64 Bit, Windows 7 Professional K 32/64 Bit, Windows 7 Professional N 32/64 Bit, Windows 7 Enterprise 32/64 Bit, Windows 7 Enterprise K 32/64 Bit, Windows 7 Enterprise N 32/64 Bit, Windows 8 Pro 32/64 Bit, Windows 8 Pro K 32/64 Bit, Windows 8 Pro N 32/64 Bit, Windows 8 Enterprise 32/64 Bit, Windows 8 Enterprise K 32/64 Bit, Windows 8 Enterprise N 32/64 Bit, Windows 8.1 Pro 32/64 Bit, Windows 8.1 Pro K 32/64 Bit, Windows 8.1 Pro N 32/64 Bit, Windows 10 Pro 32/64 Bit, Windows 10 Pro N 32/64 Bit, Windows 10 Education, Ubuntu 19 and Ubuntu 21, Windows 11 Education, Windows 11 pro, Windows server 2022 standard
- Special purpose facilities available (Conduct of online Meetings/Webinars/Workshops, etc.)
- Facilities for conduct of classes/courses in online mode(Theory&Practical)
- Innovation Cell-Avialable
- Social Media Cell- Available

15.3 Compliance of the National Academic Depository (NAD), applicable to PGCM/ PGDM Institutions and University Departments

15.4 List of facilities available

● Games and Sports Facilities
SPORTS INFRASTRUCTURE

Facility	Quantity (No's)	Area/size(mx m) include extra space	Year of establishment
Playground	3	16187sqm	1997
Basketballcourt	1	32X19	1997
Volleyballcourt	1	24X15	1997
Handball	1	40X20	1997
Throwballcourt	1	22X15	1997
Kabaddicourt	2	30X24	1997
Kho-kho	1	30X30	1997
Netball	1	40X25	1997
Cricketground	1	60mts5acers	1997
Footballground	1	110X70	1997
Athletictrack200m	1	5acers	1997
Indoor		Multi-purpose Indoorstadium	2008
Multi gym	1	50sqm	2008
Shuttlebadmintoncourt	2	446sqm	2008
Chess	5		1997
Tabletennis	1		2008
Caroms	1		2008

+5.5 List of facilities available

● Games and Sports Facilities +Description
SPORTS INFRASTRUCTURE

Facility	Quantity (No's)	Area/size(mx m) include extra space	Year of establishment
Playground	3	16187sqm	1997
Basketballcourt	1	32X19	1997
Volleyballcourt	1	24X15	1997
Handball	1	40X20	1997
Throwballcourt	1	22X15	1997
Kabaddicourt	2	30X24	1997
Kho-kho	1	30X30	1997
Netball	1	40X25	1997
Cricketground	1	60mts5acers	1997
Footballground	1	110X70	1997
Athletictrack200m	1	5acers	1997

Indoor		Multi-purpose Indoorstadium	2008
Multi gym	1	50sqm	2008
Shuttlebadmintoncourt	2	446sqm	2008
Chess	5		1997
Tabletennis	1		2008
Caroms	1		2008







● Extra-Curricular Activities: Yoga center

Teaching Learning Process (TLP)

●Curriculum and syllabus for each of the Programmes as approved by the University

Sl. No.	BRANCH	Scheme	Link	
1	Electronics & Communication Engineering	2018	https://vtu.ac.in/pdf/2018syll/is.pdf  2018SCHEMESYLLABUS.pdf	
		2021	https://vtu.ac.in/pdf/2021syll/isesch.pdf  2021SYLLABUS.pdf	
		2022	https://vtu.ac.in/pdf/2022_3to8/2ecesyll.pdf	
		2025	https://vtu.ac.in/en/b-e-scheme-syllabus/	
2	Computer Science & Engineering	2018	https://vtu.ac.in/pdf/2018syll/cs.pdf	
		2021	https://vtu.ac.in/pdf/2021syll/csesch.pdf	
		2022	https://vtu.ac.in/pdf/2022_3to8/2csesyll.pdf	
			https://vtu.ac.in/pdf/2022_3to8/3csesyll.pdf	
			https://vtu.ac.in/pdf/2022_3to8/6csesyll.pdf https://vtu.ac.in/pdf/2022_3to8/7csesyll.pdf	
2025	https://vtu.ac.in/en/b-e-scheme-syllabus/			
3	Information Science & Engineering	2018	https://vtu.ac.in/pdf/2018syll/is.pdf	
		2021	https://vtu.ac.in/pdf/2021syll/isesch.pdf	
		2022	https://vtu.ac.in/pdf/2022_3to8/2issyll.pdf https://vtu.ac.in/pdf/2022_3to8/3issyll.pdf https://vtu.ac.in/pdf/2022_3to8/6issyll.pdf https://vtu.ac.in/pdf/2022_3to8/7issyll.pdf	
			2025	https://vtu.ac.in/en/b-e-scheme-syllabus/
4	Artificial Intelligence & Machine Learning	2018	https://vtu.ac.in/pdf/2018syll/me.pdf	
		2021	https://vtu.ac.in/pdf/2021syll/mesch.pdf	
		2022	https://vtu.ac.in/pdf/2022_3to8/2aimlsyll.pdf https://vtu.ac.in/pdf/2022_3to8/3aimlsyll.pdf https://vtu.ac.in/pdf/2022_3to8/7aimlsyll.pdf	
			2025	https://vtu.ac.in/en/b-e-scheme-syllabus/
5	Mechanical Engineering	2018	https://vtu.ac.in/pdf/2018syll/cv.pdf	
		2021	https://vtu.ac.in/pdf/2021syll/cvsch.pdf	
		2022	https://vtu.ac.in/pdf/2022_3to8/2mecsyll.pdf https://vtu.ac.in/pdf/2022_3to8/3mecsyll.pdf https://vtu.ac.in/pdf/2022_3to8/4mecsyll.pdf	
			2025	https://vtu.ac.in/en/b-e-scheme-syllabus/

6	Civil Engineering	2018	https://vtu.ac.in/pdf/2018syll/ai.pdf
		2021	https://vtu.ac.in/pdf/2021syll/aisch.pdf
		2022	https://vtu.ac.in/pdf/2022_3to8/civsch.pdf https://vtu.ac.in/pdf/2022_3to8/58civsch.pdf https://vtu.ac.in/pdf/2022_3to8/2civsyll.pdf https://vtu.ac.in/pdf/2022_3to8/3civsyll.pdf https://vtu.ac.in/pdf/2022_3to8/4civsyll.pdf
		2025	https://vtu.ac.in/en/b-e-scheme-syllabus/

For each Post Graduate Course give the following:

● Title of the Course

-NA-

● Curricula and Syllabi

-NA-

● Laboratory facilities exclusive to the Post Graduate Course

-NA-